

PPPPPPPPPPPP		AAAAAAAAAA	TTTTTTTTTTTTTTTT	CCCCCCCCCCCC	HHH	HHH
PPPPPPPPPPPP		AAAAAAAAAA	TTTTTTTTTTTTTTTT	CCCCCCCCCCCC	HHH	HHH
PPPPPPPPPPPP		AAAAAAAAAA	TTTTTTTTTTTTTTTT	CCCCCCCCCCCC	HHH	HHH
PPP	PPP	AAA	TTT	CCC	HHH	HHH
PPP	PPP	AAA	TTT	CCC	HHH	HHH
PPP	PPP	AAA	TTT	CCC	HHH	HHH
PPP	PPP	AAA	TTT	CCC	HHH	HHH
PPP	PPP	AAA	TTT	CCC	HHH	HHH
PPP	PPP	AAA	TTT	CCC	HHH	HHH
PPPPPPPPPPPP		AAA	TTT	CCC	HHH	HHH
PPPPPPPPPPPP		AAA	TTT	CCC	HHHHHHHHHHHHHHHH	HHHHHHHHHHHHHHHH
PPPPPPPPPPPP		AAA	TTT	CCC	HHHHHHHHHHHHHHHH	HHHHHHHHHHHHHHHH
PPP		AAAAAAAAAAAAAAAA	TTT	CCC	HHH	HHH
PPP		AAAAAAAAAAAAAAAA	TTT	CCC	HHH	HHH
PPP		AAAAAAAAAAAAAAAA	TTT	CCC	HHH	HHH
PPP		AAA	TTT	CCC	HHH	HHH
PPP		AAA	TTT	CCC	HHH	HHH
PPP		AAA	TTT	CCC	HHH	HHH
PPP		AAA	TTT	CCC	HHH	HHH
PPP		AAA	TTT	CCCCCCCCCCCC	HHH	HHH
PPP		AAA	TTT	CCCCCCCCCCCC	HHH	HHH
PPP		AAA	TTT	CCCCCCCCCCCC	HHH	HHH

PA1
VO4[illegible][illegible]

```
1 0001 0 MODULE PATINT (
2 L 0002 0 %IF %VARIANT EQL 1
3 0003 0 %THEN
4 0004 0 ADDRESSING_MODE (EXTERNAL = LONG_RELATIVE, NONEXTERNAL = LONG_RELATIVE),
5 0005 0 %FI
6 0006 0 IDENT = 'V04-000'
7 0007 0 ) =
8 0008 1 BEGIN
9 0009 1
10 0010 1
11 0011 1 *****
12 0012 1 *
13 0013 1 * COPYRIGHT (c) 1978, 1980, 1982, 1984 BY
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30 0030 1 *
31 0031 1 *
32 0032 1 *****
33 0033 1
34 0034 1
35 0035 1 ++
36 0036 1 FACILITY: PATCH
37 0037 1
38 0038 1 ABSTRACT: This is the RST/DST/PATCH interface module.
39 0039 1 This module exists because the DST/RST
40 0040 1 module simply declares how it wants to see
41 0041 1 the world, and leaves it up to this module
42 0042 1 to interface to PATCH to make things look
43 0043 1 that way.
44 0044 1
45 0045 1 This module defines the interface between the
46 0046 1 PATCH RST builder/manipulator and the LINKER-produced
47 0047 1 DST. The former would like to show as little
48 0048 1 concern for where DST records are actually stored as
49 0049 1 possible. The latter would like to provide this facility,
50 0050 1 but it must do so simply, (because we don't want to
51 0051 1 re-invent the world), efficiently, and in such
52 0052 1 as way as to allow us to do radically different
53 0053 1 things about where the DST actually exists.
54 0054 1
55 0055 1 Essentially what we do to solve this is to restrict the
56 0056 1 DST user to requesting records before he uses them,
57 0057 1 (probably) saying something about how long he wants
```


58 0058 1 to use them (or, equivalently, when he is willing to give
59 0059 1 them up), and using them given that they exist at the
60 0060 1 address he is told they are currently at. This means that
61 0061 1 he can never make any assumptions about where a record is at.
62 0062 1 To get around this we introduce the concept of 'Record Ids',
63 0063 1 which are simply identifiers by which the two sides of the
64 0064 1 interface agree to call records. The first time you
65 0065 1 get a record, the interface tells you how you must
66 0066 1 henceforth refer to it.
67 0067 1
68 0068 1 The other aspect of the interface concerns so-called
69 0069 1 RST-pointers. These pointers are used through the
70 0070 1 RST module to access various (all) records. The code
71 0071 1 uses these pointers implicitly, knowing nothing
72 0072 1 about what they actually are, and leaves it up to this
73 0073 1 interface to define them. This is done by
74 0074 1 having a special storage allocator for the RST
75 0075 1 module. It uses whatever kind of pointer this
76 0076 1 allocator returns, and leaves it up to
77 0077 1 the definition of the RST structures (RST_NT,
78 0078 1 RST_MC, etc. see PATRST.REQ) to make
79 0079 1 sure that these RST-pointers do the job.
80 0080 1
81 0081 1
82 0082 1 ENVIRONMENT: This module runs on VAX under STARLET, user mode, non-AST level.
83 0083 1
84 0084 1 AUTHOR: Kevin Pammett, CREATION DATE: 12 JULY 77
85 0085 1
86 0086 1 MODIFIED BY:
87 0087 1
88 0088 1 V03-005 MCN0157 Maria del C. Nasr 20-Mar-1984
89 0089 1 Remove any references to OLCRAB since it is not used.
90 0090 1
91 0091 1 V03-004 MCN0151 Maria del C. Nasr 13-Feb-1984
92 0092 1 Add qualifier VOLATILE to local variable GL_SYM_COUNT to
93 0093 1 informational messages from the compiler.
94 0094 1
95 0095 1 V03-003 MTR0017 Mike Rhodes 15-Nov-1982
96 0096 1 Correct the 'next entry point' address computations for
97 0097 1 GSD\$C_EPM and GSD\$C_PRO type symbol definitions in routine
98 0098 1 PAT\$GET_NXT_GST.
99 0099 1
100 0100 1 V03-002 MTR0012 Mike Rhodes 16-Aug-1982
101 0101 1 Modify file names to remove duplicate file name usage
102 0102 1 between code and require files.
103 0103 1
104 0104 1 V03-001 MTR0007 Mike Rhodes 14-Jun-1982
105 0105 1 Use shared system messages. Affected modules include:
106 0106 1 DYNMEM.B32, PATBAS.B32, PATCMD.B32, PATIHD.B32, PATINT.B32,
107 0107 1 PATIO.B32, PATMAI.B32, PATMSG.MSG, PATWRT.B32, and PATSPA.B32.
108 0108 1
109 0109 1 The shared messages are defined by DYNMEM.B32's invocation of
110 0110 1 SHRMSG.REQ and we simply link against these symbols. They are
111 0111 1 declared as external literals below.
112 0112 1
113 0113 1 V02-017 MTR0002 Mike Rhodes 30-Nov-1981
114 0114 1 Modify routine PAT\$GET_NXT_GST to skip global symbol

definitions for PSECT definition in shareable images.

V02-016 MTR0001 Mike Rhodes 14-Oct-1981

Modify routine PAT\$FIND_DST to allow the create and map
section system service to do expand region calls, instead
of trying to remember the last mapped address in P0 space.
The last mapped address array is updated within the calls.

V02-015 PCG0001 Peter George 02-FEB-1981

Add require statement for LIB\$:PATDEF.REQ

NO	DATE	PROGRAMMER	PURPOSE
--	----	-----	-----
00	13-DEC-77	K.D. MORSE	ADAPT VERSION 19 FOR PATCH.
01	2-JAN-78	K.D. MORSE	ALLOW NO GST IN IMAGE.
02	23-JAN-78	K.D. MORSE	ADD CODE FOR MORE SPECIFIC ERROR MESSAGES. (20)
03	28-FEB-78	K.D. MORSE	SAVE SCOPE NOW DOES A SET MODULE ON THE SCOPE'S MODULE. PAT\$FIND_DST now maps the GST instead of reading it. (22) Added routine POSITION_GST to chain through the mapped GST. Also the logic in DBG\$GET_NXT_GST now calls POSITION_GST. (23)
04	06-APR-78	K.D. MORSE	Bug fix in FIND_DST to skip the first 2 GST records OK. (24) Bug fix in POSITION_GST - round up a record byte count. (24) GSR_NEXT_ADDR is now a REF VECTOR[,byte]. (24) Added code to BUILD_PATH to check for DEFINE symbols before consulting the RST. BUILD_PATH has the final word on whether a symbol has a value or not. (25) None for vers 26.
05	25-APR-78	K.D. MORSE	CONVERT TO NATIVE COMPILER.
06	17-MAY-78	K.D. MORSE	ERROR MESSAGES FROM GST/DST INIT. ARE NOW INFOR SEVERITY. (27) POSITION GST CHECKS FOR NO GST (27). NO CHANGES FOR VERS 28. DELETE_PATH IS GLOBAL AND HAS NO FORMAL_INPUT AND ALWAYS ZEROS THE PATH_VEC_PTR. (29)
07	18-MAY-78	K.D. MORSE	BETTER ERROR-MSG IN SAVE_SCOPE (30). CANCEL THE SCOPE IF THE MODULE IT POINTS TO IS CANCELLED. (31) POSITION GST NOW SEES GST AS 3 RECORDS LESS THAN HEADER SAYS NOT 2. (32) NOTE THE "ROUND UP" IN GET_NXT_GST TO RECOGNIZE END OF GST RECORD. (32)
08	24-MAY-78	K.D. MORSE	NO CHANGES FOR VERS 33. ADD GSD TYPE 3 - PROCEDURE DEFINITION WITH FORMAL ARGUMENT DESCRIPTIONS.

PATINT
V04-000

I 15
16-Sep-1984 01:02:56
14-Sep-1984 12:52:34

VAX-11 Bliss-32 V4.0-742
DISK\$VMSMASTER:[PATCH.SRC]PATINT.B32;1 Page 4 (1)

172	0172	1	09	25-MAY-78	K.D. MORSE
173	0173	1			
174	0174	1			
175	0175	1	10	13-JUN-78	K.D. MORSE
176	0176	1	11	20-JUN-78	K.D. MORSE
177	0177	1	12	28-JUN-78	K.D. MORSE
178	0178	1			
179	0179	1			
180	0180	1			
181	0181	1	13	29-JUN-78	K.D. MORSE
182	0182	1	14	07-JUL-78	K.D. MORSE
183	0183	1			
184	0184	1	--		

ADD SIGNAL FLAG PARAMETER TO
PAT\$BUILD PATH FOR FORWARD
REFERENCED SYMBOLS.
ADD FAO COUNT TO SIGNALS.
NO CHANGES FOR VERS 34-36.
NO CHANGES FOR 37-38.
PAT\$FIND MODULE HAS NEW ARG
INDICATING WHETHER OR NOT TO
SIGNAL IF MODULE IS NOT FOUND (39).
NO CHANGES FOR VERS 40.
NO CHANGES FOR VERS 41.

```
186 0185 1 |
187 0186 1 | TABLE OF CONTENTS:
188 0187 1 |
189 0188 1 |
190 0189 1 | FORWARD ROUTINE
191 0190 1 | PAT$SAVE_SCOPE.
192 0191 1 |
193 0192 1 | PAT$BUILD_PATH.
194 0193 1 |
195 0194 1 | PAT$DELETE_PATH : NOVALUE.
196 0195 1 | PAT$FIND_MODULE.
197 0196 1 | PAT$RST_FREEZ.
198 0197 1 |
199 0198 1 | PAT$RST_RELEASE : NOVALUE.
200 0199 1 |
201 0200 1 | PAT$FIND_DST : NOVALUE.
202 0201 1 | PAT$GET_DST_REC.
203 0202 1 | PAT$POSITON_DST.
204 0203 1 |
205 0204 1 | POSITION GST.
206 0205 1 | PAT$GET_NXT_DST.
207 0206 1 | PAT$GET_NXT_GST;
208 0207 1 |
209 0208 1 |
210 0209 1 |
211 0210 1 | INCLUDE FILES:
212 0211 1 |
213 0212 1 |
214 0213 1 | LIBRARY 'SYSS$LIBRARY:LIB.L32';
215 0214 1 | REQUIRE 'SRC$:PATPCT.REQ';
216 0254 1 | REQUIRE 'LIB$:PATDEF.REQ';
217 0308 1 | REQUIRE 'LIB$:PATMSG.REQ';
218 0482 1 | REQUIRE 'SRC$:IMGDEF.REQ';
219 0549 1 | REQUIRE 'SRC$:PATGEN.REQ';
220 0771 1 | REQUIRE 'SRC$:BSTRUC.REQ';
221 0847 1 | REQUIRE 'SRC$:LISTEL.REQ';
222 0889 1 | REQUIRE 'SRC$:DLLNAM.REQ';
223 0947 1 | REQUIRE 'SRC$:PATRTS.REQ';
224 2043 1 | REQUIRE 'SRC$:VXSMAC.REQ';
225 2108 1 | REQUIRE 'SRC$:SYSSER.REQ';
```

```
| Store away the current scope position
| (CSP) vector.
| Collect symbol pathnames and eventually
| try to evaluate them.
| Throw away a pathvector.
| Scan MC for a given module name.
| Storage allocator for anything which
| which is accessed via RST-pointers.
| Storage deallocator for anything which
| which is allocated by PAT$RST_FREEZ.
| Find the DST and make it available.
| Make a certain DST record available.
| Make a certain DST record available
| and set up for PAT$GET_NXT_DST
| Make a certain GST record available
| Make the next DST record available.
| Make the next GST record available
```

! Defines literals

PATINT
V04-000

K 15
16-Sep-1984 01:02:56
15-Sep-1984 22:50:49

VAX-11 BLISS-32 V4.0-742
_S255SDUA28:[PATCH.SRC]SYSSER.REQ;1

Page 6
(1)

; R2140 1
; R2141 1
; R2142 1
; R2143 1
; R2144 1

SWITCHES LIST (SOURCE);

EXTERNAL ROUTINE
PAT\$fa0_out;

! formats a line and outputs to the terminal

PA
V0

PATINT
V04-000

M 15
16-Sep-1984 01:02:56
14-Sep-1984 12:52:34

VAX-11 Bliss-32 V4.0-742 Page 8
DISK\$VMSMASTER:[PATCH.SRC]PATINT.B32;1 (2)

283	2645	1	PAT\$GL_RST_BEGN,	! Address of start of RST
284	2646	1	PAT\$GL_HEAD_LST,	! Head of PATCH argument list
285	2647	1	PAT\$GL_SYMTBPTR,	! Pointer to current symbol table
286	2648	1	PAT\$GL_SYMHEAD;	! Pointer to user-defined symbol table l'sth
287	2649	1		
288	2650	1	EXTERNAL LITERAL	
289	2651	1		
290	2652	1	Define shared message references. (resolved @ link time)	
291	2653	1		
292	2654	1	PAT\$_CLOSEIN,	! Error closing input file.
293	2655	1	PAT\$_CLOSEOUT,	! Error closing output file.
294	2656	1	PAT\$_OPENIN,	! Error opening input file.
295	2657	1	PAT\$_OPENOUT,	! Error opening output file.
296	2658	1	PAT\$_READERR,	! Error reading from file.
297	2659	1	PAT\$_SYSEERROR,	! System Service error.
298	2660	1	PAT\$_WRITEERR;	! Error writing to file.
299	2661	1		
300	2662	1		

```
302 2663 1 GLOBAL ROUTINE PAT$BUILD_PATH( SYMBOL_DESC, PASS_BACK_VALUE, SIGNAL_FLAG ) =
303 2664 1
304 2665 1 ++
305 2666 1 Functional Description:
306 2667 1
307 2668 1 This routine serves two fairly distinct purposes.
308 2669 1
309 2670 1 1. If SYMBOL_DESC is a valid string descriptor, (ie not = 0),
310 2671 1 then the call was made to BUILD_PATH so that it could
311 2672 1 accumulate the elements of a pathname in order to
312 2673 1 build up a pathname vector.
313 2674 1
314 2675 1 2. Otherwise, the 0 SYMBOL_DESC is a flag which signals that
315 2676 1 no more elements are to come and what we have accumulated
316 2677 1 is supposedly a complete pathname. What we are to do then
317 2678 1 is to simply look up this pathname in the RST data base and
318 2679 1 return the corresponding value via the PASS_BACK_VALUE pointer.
319 2680 1
320 2681 1 When a lookup is done, the following priority is observed:
321 2682 1
322 2683 1 1) a pathname consisting of 1 element may first be:
323 2684 1 1) a permanent symbol name (e.g. 'R0')
324 2685 1 2) a DEFINE symbol
325 2686 1 2) if 1), above, is not the case, or if the pathname
326 2687 1 is longer than 1 element, then the symbol must
327 2688 1 be found in the RST or an error occurs.
328 2689 1
329 2690 1 Calling Sequence:
330 2691 1
331 2692 1 PAT$BUILD_PATH ( SYMBOL_DESC, PASS_BACK_VALUE, SIGNAL_FLAG)
332 2693 1
333 2694 1 Inputs:
334 2695 1
335 2696 1 SYMBOL_DESC - String descriptor for next peice of pathname or
336 2697 1 zero indicating accumulated pathname is to be
337 2698 1 evaluated.
338 2699 1 PASS BACK VALUE - Address of where to return the symbol's value
339 2700 1 SIGNAL_FLAG - Flag indicating whether to signal error message
340 2701 1 if symbol is undefined. (TRUE=yes, FALSE=no)
341 2702 1
342 2703 1 Implicit Inputs:
343 2704 1
344 2705 1 This routine works from the OWN that is local to this
345 2706 1 module, PATH_VEC_PTR, which points to the current pathname vector
346 2707 1 we are building. The reason why this is not local to BUILD_PATH
347 2708 1 is so that it can be shared by SAVE_SCOPE.
348 2709 1
349 2710 1 Return Value:
350 2711 1
351 2712 1 On pathname accumulation, we return TRUE unless some error
352 2713 1 like PATCH running out of free storage occurs; then an error is SIGNALed.
353 2714 1
354 2715 1 On symbol evaluation, we return TRUE if the symbol was found
355 2716 1 in the image symbol tables and PAT$K_USER_DEF if the symbol was found
356 2717 1 in the user-defined symbol table. If the symbol is undefined,
357 2718 1 then depending upon SIGNAL_FLAG either an error message is SIGNALed
358 2719 1 and an UNWIND is done, or PAT$BUILD_PATH returns FALSE. This is to
```



```
359 2720 1 | handle forward references inside symbolic instructions.
360 2721 1 | --
361 2722 1 |
362 2723 2 BEGIN
363 2724 2 |
364 2725 2 MAP
365 2726 2 | SYMBOL_DESC : REF BLOCK[.BYTE],
366 2727 2 | This describes the element of the
367 2728 2 | pathname which we are to add on
368 2729 2 | to our list.
369 2730 2 | This is where we are to pass back
370 2731 2 | the pathname value to.
371 2732 2 |
372 2733 2 OWN
373 2734 2 | PV_INDEX;
374 2735 2 | We use an OWN index into the OWN
375 2736 2 | pathname vector so that on each call
376 2737 2 | we know where we're at.
377 2738 2 LOCAL
378 2739 2 | CS_PTR : CS_POINTER,
379 2740 2 | STATUS;
380 2741 2 | Temp counted string pointer.
381 2742 2 | Success/failure indication that we return.
382 2743 2 |
383 2744 2 | ++
384 2745 2 | Now see whether a pathname translation to symbolic value
385 2746 2 | is to occur. This is signaled by the flag SYMBOL_DESC being
386 2747 2 | equal to 0.
387 2748 2 | --
388 2749 2 | IF (.SYMBOL_DESC EQL 0)
389 2750 2 | THEN
390 2751 2 | BEGIN
391 2752 2 | | ++
392 2753 2 | | Evaluate the symbol. First, for single-element pathnames we give
393 2754 2 | | priority to the so-called PATCH permanent symbols, and to the symbols
394 2755 2 | | defined by the user at PATCH-time. No longer pathname could be such
395 2756 2 | | a thing.
396 2757 2 | | --
397 2758 2 | | STATUS = 0;
398 2759 2 | | IF (.PATH_VEC_PTR[1] EQL 0)
399 2760 2 | | THEN
400 2761 2 | | | BEGIN
401 2762 2 | | | LOCAL
402 2763 2 | | | TEMP_SYM_TBL,
403 2764 2 | | | DEF_SYM_DESC : BLOCK[8,BYTE];
404 2765 2 | | | ++
405 2766 2 | | | A 1-element pathname may be or a DEFINE symbol. First build
406 2767 2 | | | a string descriptor for the name (since this is what
407 2768 2 | | | PAT$FIND_SYM wants).
408 2769 2 | | | --
409 2770 2 | | | CS_PTR = .PATH_VEC_PTR[0];
410 2771 2 | | | DEF_SYM_DESC[DSC$W_LENGTH] = .CS_PTR[0];
411 2772 2 | | | DEF_SYM_DESC[DSC$A_POINTER] = CS_PTR[1];
412 2773 2 | | | ++
413 2774 2 | | | The symbol is not a permanent one. Now lookup it up in the
414 2775 2 | | | linked list reserved for DEFINE symbols.
415 2776 2 | | | --
416 2777 2 | | | TEMP_SYM_TBL = .PAT$GL_SYMTBPTR;
417 2778 2 | | | ! Remember current symbol table
```

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```
PAT$GL_SYMTBPTR = .PAT$GL_SYMHEAD;  
STATUS = PAT$FIND_SYM(DEF_SYM_DESC);  
PAT$GL_SYMTBPTR = .TEMP_SYM_TBL;
```

! Use user-defined symbol table
! Restore current symbol table

```
++  
If we found something, pass back the associated value  
and set STATUS to the appropriate success code.
```

```
--  
IF (.STATUS NEQ 0)  
THEN  
    BEGIN  
        PASS_BACK_VALUE[0] = .SYM_VALUE(.STATUS);  
        STATUS = PAT$K_USER_DEF;  
    END;
```

```
END;
```

```
++  
Now, if we didn't get something from the DEFINE  
or permanent symbol data bases, try the RST.
```

```
--  
IF (NOT .STATUS)  
THEN  
    STATUS = PAT$SYM_TO_VAL( .PATH_VEC_PTR, .PASS_BACK_VALUE);
```

```
++  
If no translation can be found, check whether to SIGNAL an error  
and cause an UNWIND or return FALSE.
```

```
--  
IF (NOT .STATUS)  
THEN  
    BEGIN  
        LOCAL MESSAGE_BUF : VECTOR[TTY_OUT_WIDTH,BYTE];
```

```
++  
Encode the pathname into a counted  
string, and output the associated message.
```

```
--  
PAT$PV TO CS( .PATH_VEC_PTR, MESSAGE_BUF );  
PAT$DELETE_PATH();  
PATH_VEC_PTR = 0;
```

```
++  
Check if this might be a forward reference and therefore  
should not be signaled as an error.
```

```
--  
IF (NOT .SIGNAL_FLAG)  
THEN  
    RETURN(FALSE)  
ELSE  
    SIGNAL(PAT$NOSYMBOL, 1, MESSAGE_BUF ); ! no return  
END;
```

```
++  
If the evaluation succeeded, discard the pathname vector and  
return success.
```

```
--  
PAT$DELETE_PATH();
```

```
473 2834 RETURN(.STATUS);
474 2835 END;
475 2836
476 2837 ++
477 2838 A real string descriptor is supposed to pass on to us another pathname
478 2839 element to accumulate.
479 2840
480 2841 If this is the first call for a new pathname, we must allocate the storage
481 2842 we will need for the vector of pointers to the element strings.
482 2843 --
483 2844 IF (.PATH_VEC_PTR EQL 0)
484 2845 THEN
485 2846 BEGIN
486 2847 IF ((PATH_VEC_PTR = PAT$freez(RST_UNITS(%SIZE(PATHNAME_VECTOR)))) EQL 0)
487 2848 THEN
488 2849 SIGNAL(PAT$_NOFREE); ! No more storage.
489 2850
490 2851 ++
491 2852 The storage manager zeros out the pathname vector for us, so we only
492 2853 have to set up the right pathname vector index.
493 2854 --
494 2855 PV_INDEX = 0;
495 2856 END;
496 2857
497 2858 ++
498 2859 Now we need space for the element name itself, (including the count! ).
499 2860 --
500 2861 IF ((CS_PTR = PAT$freez(RST_UNITS(.SYMBOL_DESC[DSC$W_LENGTH]+1))) EQL 0)
501 2862 THEN
502 2863 SIGNAL(PAT$_NOFREE); ! No more storage.
503 2864
504 2865 ++
505 2866 Copy the string into the allocated storage. Note that we must make up a counted
506 2867 string because this is what pathname vector pointers are defined to point to.
507 2868 --
508 2869 CS_PTR[0] = .SYMBOL_DESC[DSC$W_LENGTH];
509 2870 CH$MOVE(.SYMBOL_DESC[DSC$W_LENGTH], .SYMBOL_DESC[DSC$A_POINTER], CS_PTR[1]);
510 2871
511 2872 ++
512 2873 Now store the address of this counted string in the 'next' slot in the
513 2874 pathname vector.
514 2875 --
515 2876 PATH_VEC_PTR[PV_INDEX] = .CS_PTR;
516 2877
517 2878 ++
518 2879 And set up so that the next call to this routine stores the CS pointer into the
519 2880 next slot.
520 2881 --
521 2882 IF ((PV_INDEX = .PV_INDEX + 1) GTR MAX_PATH_SIZE)
522 2883 THEN
523 2884 BEGIN
524 2885 SIGNAL (PAT$ PATHTLONG);
525 2886 RETURN(FALSE);
526 2887 END;
527 2888 RETURN(TRUE);
528 2889 END;
```



```
.TITLE PATINT
.IDENT \V04-000\

.PSECT _PAT$OWN,NOEXE,2

00000000 00000 PATH_VEC_PTR:
          .LONG 0
          00004 DST_BEGIN_ADDR:
          .BLKB 4
          00008 DST_END_ADDR:
          .BLKB 4
          0000C DST_NEXT_ADDR:
          .BLKB 4
          00010 GSR_BEGIN_ADDR:
          .BLKB 4
          00014 GSR_NEXT_ADDR:
          .BLKB 4
          00018 GST_BEGIN_ADDR:
          .BLKB 4
          0001C GSD_REC_COUNT:
          .BLKB 4
          00020 PV_INDEX:
          .BLKB 4

ISE$C_SIZE== 20
TXT$C_SIZE== 4
PAL$C_SIZE== 16
ASD$C_SIZE== 9
FWR$C_SIZE== 24

.EXTRN PAT$FAO OUT, PAT$PV TO CS
.EXTRN PAT$FIND SYM, PAT$SET MODULE
.EXTRN PAT$SYM TO VAL, PAT$SYM TO VALU
.EXTRN PAT$INIT RST, PAT$FREEZ
.EXTRN PAT$FREEERELASE
.EXTRN LIB$CREMAPSEC, PAT$GB SYMBOLS
.EXTRN PAT$GL_IMGHDR, PAT$GL_OLDNBK
.EXTRN PAT$GL_OLDNAME, PAT$GL_ISVADDR
.EXTRN PAT$GL_CSP_PTR, PAT$GL_MC_PTR
.EXTRN PAT$GL_RST-BEGIN
.EXTRN PAT$GL_HEAD LST
.EXTRN PAT$GL_SYMTBPTR
.EXTRN PAT$GL_SYMHEAD, PAT$ CLOSEIN
.EXTRN PAT$ CLOSEOUT, PAT$ OPENIN
.EXTRN PAT$ OPENOUT, PAT$ READERR
.EXTRN PAT$ SYSERROR, PAT$ WRITEERR
.WEAK ACCESS_CHECK

.PSECT _PAT$CODE,NOVRT,2

OFFC 00000
.ENTRY PAT$BUILD PATH, Save R2,R3,R4,R5,R6,R7,R8,- : 2663
          R9,R10,R11
          MOVAB PAT$FREEZ, R11
          MOVAB PAT$DELETE PATH, R10
          MOVAB PAT$GL_SYMTBPTR, R9
          MOVAB LIB$SIGNAL, R8
          MOVAB PATH_VEC_PTR, R7
```

5E	FF7C	CE	9E	00025	MOVAB	-132(SP), SP	
52	04	AC	D0	0002A	MOVL	SYMBOL_DESC, R2	2746
		79	12	0002E	BNEQ	3\$	
		54	D4	00030	CLRL	STATUS	2755
50		67	D0	00032	MOVL	PATH_VEC_PTR, R0	2756
	04	A0	D5	00035	TSTL	4(R0)	
		32	12	00038	BNEQ	1\$	
56		60	D0	0003A	MOVL	(R0), CS_PTR	2768
7C		66	9B	0003D	MOVZBW	(CS_PTR), DEF_SYM_DESC	2769
FC	01	A6	9E	00041	MOVAB	1(R0), DEF_SYM_DESC+4	2770
53		69	D0	00046	MOVL	PAT\$GL_SYMTBPTR, TEMP_SYM_TBL	2776
69	00000000G	EF	D0	00049	MOVL	PAT\$GL_SYMHEAD, PAT\$GL_SYMTBPTR	2777
		AE	9F	00050	PUSHAB	DEF_SYM_DESC	2778
		01	FB	00053	CALLS	#1, PAT\$FIND_SYM	
00000000G		50	D0	0005A	MOVL	R0, STATUS	
		53	D0	0005D	MOVL	TEMP_SYM_TBL, PAT\$GL_SYMTBPTR	2779
		54	D5	00060	TSTL	STATUS	2785
		08	13	00062	BEQL	1\$	
08	BC	A4	D0	00064	MOVL	8(STATUS), @PASS_BACK_VALUE	2788
		03	D0	00069	MOVL	#3, STATUS	2789
		54	E8	0006C	BLBS	STATUS, 2\$	2797
		AC	DD	0006F	PUSHL	PASS_BACK_VALUE	2799
		67	DD	00072	PUSHL	PATH_VEC_PTR	
00000000G		02	FB	00074	CALLS	#2, PAT\$SYM_TO_VAL	
		50	D0	0007B	MOVL	R0, STATUS	
		54	E8	0007E	BLBS	STATUS, 2\$	2805
		5E	DD	00081	PUSHL	SP	2814
		67	DD	00083	PUSHL	PATH_VEC_PTR	
00000000G		02	FB	00085	CALLS	#2, PAT\$PV_TO_CS	
		00	FB	0008C	CALLS	#0, PAT\$DELETE_PATH	2815
		67	D4	0008F	CLRL	PATH_VEC_PTR	2816
		AC	E9	00091	BLBC	SIGNAL_FLAG, 8\$	2822
		5E	DD	00095	PUSHL	SP	2826
		01	DD	00097	PUSHL	#1	
	006D8090	8F	DD	00099	PUSHL	#7176336	
68		03	FB	0009F	CALLS	#3, LIB\$SIGNAL	
6A		00	FB	000A2	CALLS	#0, PAT\$DELETE_PATH	2833
50		54	D0	000A5	MOVL	STATUS, R0	2834
		04	00	000AB	RET		
		67	D5	000A9	TSTL	PATH_VEC_PTR	2844
		16	12	000AB	BNEQ	5\$	
		0B	DD	000AD	PUSHL	#11	2847
68		01	FB	000AF	CALLS	#1, PAT\$FREEZ	
67		50	D0	000B2	MOVL	R0, PATH_VEC_PTR	
		09	12	000B5	BNEQ	4\$	
	006D8112	8F	DD	000B7	PUSHL	#7176466	2849
68		01	FB	000BD	CALLS	#1, LIB\$SIGNAL	
		A7	D4	000C0	CLRL	PV_INDEX	2855
50		62	3C	000C3	MOVZWL	(R2), R0	2861
50		04	C0	000C6	ADDL2	#4, R0	
50		04	C7	000C9	DIVL3	#4, R0, -(SP)	
68		01	FB	000CD	CALLS	#1, PAT\$FREEZ	
56		50	D0	000D0	MOVL	R0, CS_PTR	
		09	12	000D3	BNEQ	6\$	
	006D8112	8F	DD	000D5	PUSHL	#7176466	2863
68		01	FB	000DB	CALLS	#1, LIB\$SIGNAL	
66		62	90	000DE	MOVAB	(R2), (CS_PTR)	2869

PATINT
V04-000

G 16
16-Sep-1984 01:02:56
14-Sep-1984 12:52:34

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01	A6	04	B2		62	28	000E1	MOVCL	(R2), 24(R2), 1(CS_PTR)	:	2870	
			50		A7	D0	000E7	MOVL	PV_INDEX, R0	:	2876	
		00	B740	20	56	D0	000EB	MOVL	CS_PTR, @PATH_VEC_PTR[R0]	:		
	50	20	A7		C1	C1	000F0	ADDL3	,1, PV_INDEX, -R0	:	2882	
		20	A7		50	D0	000F5	MOVL	R0, PV_INDEX	:		
			0A		50	D1	000F9	CMPL	R0, #10	:		
					0B	15	000FC	BLEQ	7\$:		
				006D8152	8F	DD	000FE	PUSHL	#7176530	:	2885	
				68	01	FB	00104	CALLS	#1, LIB\$SIGNAL	:		
					04	11	00107	BRB	8\$:	2886	
				50	01	D0	00109	7\$:	MOVL	#1, R0	:	2888
						04	0010C	RET		:		
					50	D4	0010D	8\$:	CLRL	R0	:	2889
						04	0010F	RET		:		

; Routine Size: 272 bytes, Routine Base: _PAT\$CODE + 0000


```
2890 1 GLOBAL ROUTINE PAT$DELETE_PATH : NOVALUE =
2891
2892 1 ++
2893 1 Functional Description:
2894 1
2895 1 Delete the pathname vector we are passed a pointer to by the OWN,
2896 1 PATH_VEC_PTR, which several routines in this module work from. Also,
2897 1 zero out this pointer so that the next call to BUILD_PATH knows
2898 1 there is no 'current' pathname vector being built.
2899 1
2900 1 Formal Parameters:
2901 1
2902 1 none
2903 1
2904 1 Implicit Inputs:
2905 1
2906 1 PATH_VEC_PTR - See above.
2907 1
2908 1 Return Value:
2909 1
2910 1 NOVALUE - because the only thing which can go wrong
2911 1 is a free storage error and in that
2912 1 case the manager itself SIGNALs its way out.
2913 1
2914 1 --
2915 1
2916 1 BEGIN
2917 1
2918 1 LOCAL
2919 1 CS_PTR : CS_POINTER; ! Each element of the pathname vector
2920 1 ! is a pointer to a counted string.
2921 1
2922 1 ++
2923 1 Now see if there is really a pathname vector currently pointed to by the
2924 1 pointer, PATH_VEC_PTR.
2925 1 --
2926 1 IF (.PATH_VEC_PTR EQLA 0)
2927 1 THEN
2928 1 RETURN;
2929 1
2930 1 ++
2931 1 Simply throw away the storage which we allocated
2932 1 for each element of the vector.
2933 1 --
2934 1 INCR I FROM 0 TO MAX_PATH_SIZE
2935 1 DO
2936 1 ++
2937 1 The first 0 entry ends the vector.
2938 1 --
2939 1 IF ((CS_PTR = .PATH_VEC_PTR[I]) EQL 0)
2940 1 THEN
2941 1 EXITLOOP
2942 1 ELSE
2943 1 PAT$FREERELEASE( .CS_PTR, RST_UNITS(.CS_PTR[0]+1) );
2944 1
2945 1 ++
2946 1 Then throw away the vector itself.
```

```
587 2947 1--
588 2948 PAT$FREERELEASE( .PATH_VEC_PTR, RST_UNITS( %SIZE(PATHNAME_VECTOR) ));
589 2949
590 2950 1++
591 2951 Zero out the pointer so that subsequent re-uses know there is no longer
592 2952 one there.
593 2953 1--
594 2954 PATH_VEC_PTR = 0;
595 2955
596 2956 END;
```

			003C 00000	ENTRY PAT\$DELETE_PATH, Save R2,R3,R4,R5	2890
	55	00000000G	EF 9E 00002	MOVAB PAT\$FREERELEASE, R5	
	54	00000000'	EF 9E 00009	MOVAB PATH_VEC_PTR, R4	
			64 D5 00010	TSTL PATH_VEC_PTR	2926
			25 13 00012	BEQL 3\$	
			52 D4 00014	CLRL 1	2934
	53	00 B442	D0 00016 1\$:	MOVL @PATH_VEC_PTR[1], CS_PTR	2939
			13 13 0001B	BEQL 2\$	
	50		63 9A 0001D	MOVZBL (CS_PTR), R0	2943
	50		04 C0 00020	ADDL2 #4, R0	
7E	50		04 C7 00023	DIVL3 #4, R0, -(SP)	
			53 DD 00027	PUSHL CS_PTR	
	65		02 FB 00029	CALLS #2, PAT\$FREERELEASE	
E6	52		0A F3 0002C	AOBLEQ #10, 1, 1\$	2939
			0B DD 00030 2\$:	PUSHL #11	2948
			64 DD 00032	PUSHL PATH_VEC_PTR	
	65		02 FB 00034	CALLS #2, PAT\$FREERELEASE	
			64 D4 00037	CLRL PATH_VEC_PTR	2954
			04 00039 3\$:	RET	2956

; Routine Size: 58 bytes, Routine Base: _PAT\$CODE + 0110

GLOBAL ROUTINE PATSCOPE(SET_SCOPE_FLAG) =

++

Functional Description:

This routine serves two fairly distinct purposes.

1. IF SET_SCOPE_FLAG is ON, then this routine was called to SET the new current scope position (CSP). In this case we delete the storage taken by the old CSP, if there was any, and install the new CSP having checked its validity.
SET SCOPE also implies SET MODULE.
2. If SET_SCOPE_FLAG is OFF, then the call was made to simply install a null CSP vector. This happens as a result of the user cancelling scope, or cancelling a module whose name is the same as what the CSP pathname begins with. The latter avoids the 'dangling scope' problem.

Implicit Inputs:

This routine works from the OWN that is local to this module, PATH_VEC_PTR, which points to the current pathname vector which was (presumably) built by BUILD_PATH. We store away this pathname vector pointer, and then zero out the one that BUILD_PATH uses so that it 'forgets' completely about having built it.

Return Value:

TRUE, if we are simply throwing away the old CSP,
or if we installed a new one which was acceptable,
FALSE, otherwise. (we were asked to install one which was invalid).

BEGIN

LOCAL

NEW_CSP_PTR : REF PATHNAME_VECTOR,
MC_PTR : REF MC_RECORD,
CS_PTR : CS_POINTER,
STATUS;

! Used to chain along the MC.
! Temp counted string pointer.
! Success/failure indication that we return.

++

The gross structure of this routine just implements the two-function logic.

--
IF (.SET_SCOPE_FLAG)
THEN

BEGIN

++

Install a new CSP vector. Check that the CSP we were given is valid.
First, see if the beginning element of the pathvector (which must be
MODULE) is in the MC. Note that we don't consider the first entry in
the MC since it is used for globals only and hence is nameless.

--
CS_PTR = .PATH_VEC_PTR[0];


```
MC_PTR = .PAT$GL_MC_PTR;
WHILE ((MC_PTR = .MC_PTR [MC_NEXT]) NEQ 0)
DO
    BEGIN
        IF (CH$EQL(.MC_PTR[MC_NAME_CS], MC_PTR[MC_NAME_ADDR],
            .CS_PTR[0], CS_PTR[1]))
        THEN
            EXITLOOP ! Found. Continue on to do further checking
        END;

    ++
    If the module name was not found, we must not accept the CSP.
    --
    IF (.MC_PTR EQL 0)
    THEN
        BEGIN
            ++
            This is an error. Note that if there was previous to this
            call a valid CSP, it is not affected by this error. Also note
            that the storage for the CSP we just found to be invalid is
            discarded by the end-of-line processing AFTER the SIGNAL
            produces the message.
            --
            SIGNAL(PAT$ NOSUCHMODU,1,.CS_PTR);
            RETURN(FALSE);
        END;

    ++
    Make sure that the indicated module is in the RST so that
    further checking can be done and because a "set scope" implies a
    "SET MODULE" command.
    --
    IF NOT .MC_PTR[MC_IN_RST]
    THEN
        PAT$SET_MODULE(.MC_PTR); ! IF THIS FAILS, THERE IS NOT RETURN FROM TH

    ++
    The module name is valid and in the RST. Any further checking depends
    on whether the given CSP is any longer than simply "module". If this
    is the case, we've done all the validating we can.
    --
    IF (.PATH_VEC_PTR[1] NEQ 0)
    THEN
        BEGIN
            ++
            Further checking is RST-dependent.
            --
            LOCAL
                VAL_DESC : VALU_DESCRIPTOR,
                NT_PTR : REF NT_RECORD;

            ++
            For initialized modules, we can do a complete check.
            This means that we effectively do a lookup, and then
            make sure that the path leads to a symbol of type
            ROUTINE.
            --
```

```
IF (NOT PAT$SYN_TO_VALU( .PATH_VEC_PTR, VAL_DESC))
THEN
    BEGIN
        ++
        Encode the pathname into a counted string and output
        the associated message.
        --
        LOCAL MESSAGE_BUF : VECTOR[TTY_OUT_WIDTH, BYTE];
        PAT$PV TO CS(.PATH_VEC_PTR, MESSAGE_BUF);
        SIGNAL(PAT$NOSYMBOL, T, MESSAGE_BUF); ! No return
        RETURN(FALSE); !***** THIS SHOULDN'T BE NEEDED
    END;

    ++
    Now we simply have to see that the valid path leads
    to ROUTINE. First we pick up the pointer to this
    symbol's name table record.
    --
    NT_PTR = .VAL_DESC [VALU_NT_PTR];
    IF (NOT .NT_PTR[NT_TYPE] EQ[ DSC$K_DTYPE_RTN])
    THEN
        BEGIN
            ++
            A valid path, but we can't accept it as a CSP
            because perpending it to any symbol would
            never result in a valid path.
            --
            SIGNAL(PAT$BAD_CSP);
            RETURN(FALSE);
        END;
    END;

    ++
    The CSP we are to SET has been checked out OK.
    --
    NEW_CSP_PTR = .PATH_VEC_PTR;
    END;

    ++
    If we get this far, the new CSP will be accepted. First, we have to release
    the storage we used up in accumulating the pathname elements of the old CSP,
    if there was one.
    --
    IF ((.PATH_VEC_PTR = .PAT$GL_CSP_PTR) NEQ 0)
    THEN
        BEGIN
            PAT$DELETE_PATH();
        END;

        ++
        If we were only throwing away the old vector, then we must be done.
        --
        IF (NOT .SET_SCOPE_FLAG)
        THEN
            BEGIN
                PAT$GL_CSP_PTR = 0;
                RETURN(TRUE);
            END;
```

```
769      3128 2
770      3129 2
771      3130 2
772      3131 2
773      3132 2
774      3133 2
775      3134 2
776      3135 2
777      3136 2
778      3137 2
779      3138 2
780      3139 2

++
Installing a new CSP is simply a matter of saving away the pointer to the
PATHNAME VECTOR. We must also zero out the pointer to the vector which
BUILD_PATH uses to deal with these vectors, since we have effectively taken
this one away.
--
PAT$GL_CSP_PTR = .NEW_CSP_PTR;
PATH_VEC_PTR = 0;
RETURN(TRUE);
END;
```

				03FC 00000	.ENTRY	PAT\$SAVE_SCOPE, Save R2,R3,R4,R5,R6,R7,R8,-	2957
						R9	
					MOVAB	LIB\$SIGNAL, R9	
					MOVAB	PAT\$GL_CSP_PTR, R8	
					MOVAB	PAT\$GL_RST_BEGN, R7	
					MOVAB	PATH_VEC_PTR, R6	
					MOVAB	-1407SP, SP	
					BLBS	SET_SCOPE_FLAG, 1\$	3004
					BRW	9\$	
					MOVL	@PATH_VEC_PTR, CS_PTR	3013
					MOVL	PAT\$GL_MC_PTR, MC_PTR	3014
					ADDL3	PAT\$GL_RST_BEGN, MC_PTR, R0	3015
					MOVZWL	(R0), MC_PTR	
					BEQL	3\$	
					ADDL3	PAT\$GL_RST_BEGN, MC_PTR, R0	3018
					MOVZBL	12(R0), R2	
					MOVZBL	(CS_PTR), R1	3019
					CMPC5	R2, -13(R0), #0, R1, 1(CS_PTR)	
					BNEQ	2\$	
					TSTL	MC_PTR	3027
					BNEQ	4\$	
					PUSHL	CS_PTR	3037
					PUSHL	#1	
					PUSHL	#7176320	
					BRB	6\$	
					ADDL3	PAT\$GL_RST_BEGN, MC_PTR, R0	3046
					BBS	#1, 3(R0), 5\$	
					PUSHL	MC_PTR	3048
					CALLS	#1, PAT\$SET_MODULE	
					MOVL	PATH_VEC_PTR, R0	3055
					TSTL	4(R0)	
					BEQL	8\$	
					PUSHAB	VAL_DESC	3071
					PUSHL	R0	
					CALLS	#2, PAT\$SYM_TO_VALU	
					BLBS	R0, 7\$	
					PUSHL	SP	3080
					PUSHL	PATH_VEC_PTR	
					CALLS	#2, PAT\$PV_TO_CS	

		5E	DD	00097	PUSHL	SP	3081
		01	DD	00099	PUSHL	#1	
	006D8090	8F	DD	0009B	PUSHL	#7176336	
69		03	FB	000A1	CALLS	#3, LIB\$SIGNAL	
		37	11	000A4	BRB	13\$	3082
50	F8	AD	3C	000A6	MOVZWL	VAL DESC, NT_PTR	3090
50		67	C0	000AA	ADDL2	PAT\$GL_RST BEGN, R0	3091
BE	8F	A0	91	000AD	CMPB	2(R0), #190	
		0B	13	000B2	BEQL	8\$	
	006D8060	8F	DD	000B4	PUSHL	#7176288	3099
69		01	FB	000BA	CALLS	#1, LIB\$SIGNAL	
		1E	11	000BD	BRB	13\$	3100
52		66	D0	000BF	MOVL	PATH_VEC_PTR, NEW_CSP_PTR	3106
66		68	D0	000C2	MOVL	PAT\$GL_CSP_PTR, PATH_VEC_PTR	3114
		05	13	000C5	BEQL	10\$	
FEFA	CF	00	FB	000C7	CALLS	#0, PAT\$DELETE_PATH	3117
	04	AC	E8	000CC	BLBS	SET SCOPE_FLAG, 11\$	3122
		68	D4	000D0	CLRL	PAT\$GL_CSP_PTR	3125
		05	11	000D2	BRB	12\$	3126
68		52	D0	000D4	MOVL	NEW_CSP_PTR, PAT\$GL_CSP_PTR	3135
		66	D4	000D7	CLRL	PATH_VEC_PTR	3136
50		01	D0	000D9	MOVL	#1, R0	3138
			04	000DC	RET		
		50	D4	000DD	CLRL	R0	3139
			04	000DF	RET		

; Routine Size: 224 bytes, Routine Base: _PAT\$CODE + 014A

```
782 3140 1 GLOBAL ROUTINE PAT$FIND_MODULE( MOD_NAME_DESC, SIGNAL_FLAG ) =
783 3141 1
784 3142 1 !++
785 3143 1 Functional Description:
786 3144 1
787 3145 1 Search the MC to see if the given module is there.
788 3146 1
789 3147 1 Formal Parameters:
790 3148 1
791 3149 1 MOD_NAME_DESC -a string descriptor for the supposed
792 3150 1 module name.
793 3151 1 SIGNAL_FLAG -indicator whether or not this routine should
794 3152 1 SIGNAL if the module is not found
795 3153 1
796 3154 1 Implicit Inputs:
797 3155 1
798 3156 1 none.
799 3157 1
800 3158 1 Implicit Outputs:
801 3159 1
802 3160 1 none
803 3161 1
804 3162 1 Returned Value:
805 3163 1
806 3164 1 0 - if the module is not found,
807 3165 1 an MC_PTR (non-zero) to the indicated MC record, otherwise.
808 3166 1
809 3167 1 Side Effects:
810 3168 1
811 3169 1 none
812 3170 1 --
813 3171 1
814 3172 2 BEGIN
815 3173 2 MAP
816 3174 2 MOD_NAME_DESC : REF BLOCK[,BYTE]; ! The supposed module name is
817 3175 2 ! described via an SRM string descriptor.
818 3176 2
819 3177 2 LOCAL
820 3178 2 MODU_CS_NAME : VECTOR[SYM_MAX_LENGTH+1, BYTE], ! COPY OF MODULE NAME FOR NOSUCHMODU ERROR M
821 3179 2 MC_PTR : REF MC_RECORD; ! We chain along the MC via this temp pointe
822 3180 2
823 3181 2 !++
824 3182 2 Scan along the MC comparing the given string with the module name stored
825 3183 2 therein. Note that we skip the first MC record because it is reserved for
826 3184 2 globals and is therefore nameless.
827 3185 2 --
828 3186 2 MC_PTR = .PAT$GL_MC_PTR;
829 3187 3 WHILE ((MC_PTR = .MC_PTR [MC_NEXT]) NEQ 0)
830 3188 2 DO
831 3189 3 BEGIN
832 3190 4 IF (CH$EQL(.MC_PTR[MC_NAME_CS],MC_PTR[MC_NAME_ADDR],
833 3191 4 .MOD_NAME_DESC[DSC$W_LENGTH],.MOD_NAME_DESC[DSC$A_POINTER] ))
834 3192 3 THEN
835 3193 4 BEGIN
836 3194 4 !++
837 3195 4 ! Found. Internally in PATCH we agree that the 'value' of a
838 3196 4 ! module string will be the RST address of its MC record.
```

```
839 3197 4      !--
840 3198 4      RETURN(.MC_PTR);
841 3199 4      END;
842 3200 2      END;
843 3201 2
844 3202 2      !++
845 3203 2      If we fall out of the above loop, then the given module name was not found.
846 3204 2      Therefore if a SIGNAL is allowed, then construct a COUNTED_STRING pointer and
847 3205 2      pass it as the error message argument.
848 3206 2      !--
849 3207 2      IF .SIGNAL_FLAG
850 3208 2      THEN
851 3209 2          BEGIN
852 3210 2              MODU_CS_NAME[0] = .MOD_NAME_DESC[DSC$W_LENGTH];
853 3211 2              CH$MOVE(.MODU_CS_NAME[0], .MOD_NAME_DESC[DSC$A_POINTER], MODU_CS_NAME[1]);
854 3212 2              SIGNAL(PAT$_NOSUCHMODU, 1, MODU_CS_NAME[0]);      ! No return
855 3213 2          END;
856 3214 2
857 3215 2      RETURN (0);
858 3216 1      END;
```

						007C 00000		.ENTRY	PAT\$FIND MODULE, Save R2,R3,R4,R5,R6	3140
		56	00000000G	EF	9E	00002		MOVAB	PAT\$GL_RST_BEGN, R6	
		5E		20	C2	00009		SUBL2	#32, SP	
		54	00000000G	EF	D0	0000C		MOVL	PAT\$GL_MC_PTR, MC_PTR	3186
		55	04	AC	D0	00013		MOVL	MOD_NAME_DESC, R5	3191
	50	54		66	C1	00017	1\$:	ADDL3	PAT\$GL_RST_BEGN, MC_PTR, R0	3187
		54		60	3C	0001B		MOVZWL	(R0), MC_PTR	
				17	13	0001E		BEQL	2\$	
	50	54		66	C1	00020		ADDL3	PAT\$GL_RST_BEGN, MC_PTR, R0	3190
		51	0C	A0	9A	00024		MOVZBL	12(R0), R1	
04	BC	00	0D	A0	51	00028		CMPC5	R1, 13(R0), #0, @MOD_NAME_DESC, @4(R5)	
				04	B5	0002F				
				E4	12	00031		BNEQ	1\$	
		50		54	D0	00033		MOVL	MC_PTR, R0	3198
					04	00036		RET		
		1E	08	AC	E9	00037	2\$:	BLBC	SIGNAL_FLAG, 3\$	3207
		6E	04	BC	90	0003B		MOVB	@MOD_NAME_DESC, MODU_CS_NAME	3210
		50		6E	9A	0003F		MOVZBL	MODU_CS_NAME, R0	3211
01	AE	04	B5	50	28	00042		MOVCL	R0, @4(R5), MODU_CS_NAME+1	
				5E	DD	00048		PUSHL	SP	3212
				01	DD	0004A		PUSHL	#1	
				8F	DD	0004C		PUSHL	#7176320	
		00000000G	00	03	FB	00052		CALLS	#3, LIB\$SIGNAL	
				50	D4	00059	3\$:	CLRL	R0	3215
				04	0005B			RET		3216

; Routine Size: 92 bytes, Routine Base: _PAT\$CODE + 022A

```
860 3217 1 GLOBAL ROUTINE PAT$FIND_DST : NOVALUE =
861 3218 1
862 3219 1
863 3220 1 ++
864 3221 1 FUNCTIONAL DESCRIPTION:
865 3222 1 Find out where the DST begins and make it available for
866 3223 1 PAT$GET_NXT_DST and PAT$GET_DST_REC.
867 3224 1 (or make it so that these routines return EOF if no DST exists).
868 3225 1 Then do the same for the GST.
869 3226 1
870 3227 1 Calling Sequence:
871 3228 1
872 3229 1 PAT$FIND_DST()
873 3230 1
874 3231 1 FORMAL PARAMETERS:
875 3232 1
876 3233 1 none
877 3234 1
878 3235 1 IMPLICIT INPUTS:
879 3236 1
880 3237 1 The image header has been read and PAT$GL_IMGHDR points to it.
881 3238 1 The old image file is open and ready to read the DST and GST.
882 3239 1 The variables pointing to the file are:
883 3240 1 PAT$GL_OLDFAB, AND PAT$GL_OLDNAME.
884 3241 1
885 3242 1 IMPLICIT OUTPUTS:
886 3243 1
887 3244 1 none
888 3245 1
889 3246 1 COMPLETION CODES:
890 3247 1 none
891 3248 1
892 3249 1 SIDE EFFECTS:
893 3250 1
894 3251 1 The notion of 'next' DST record is initialized
895 3252 1 here so that a call to PAT$GET_NXT_DST made after
896 3253 1 a call to this routine will fetch the first record.
897 3254 1
898 3255 1 The begin and end address of the DST are also established,
899 3256 1 but only for the purposes of the interface routines.
900 3257 1 There is no explicit requirement for this from the RST's
901 3258 1 viewpoint - so long as the interface can somehow
902 3259 1 know when the last record has been passed on.
903 3260 1
904 3261 1 If anything goes wrong during the GST/DST initializations,
905 3262 1 (can't EXPREG, etc.), we output the corresponding message forcing
906 3263 1 the severity to -1-, and then continue on without the GST or DST.
907 3264 1 The exceptions to this are that there must be symbol table info in
908 3265 1 the header (even if what's there is simply a pointer to say that
909 3266 1 there is no DST or GST).
910 3267 1 --
911 3268 1
912 3269 2 BEGIN
913 3270 2
914 3271 2 BIND
915 3272 2
916 3273 2 SYM_TBL_DATA = .PAT$GL_IMGHDR + .PAT$GL_IMGHDR [IHDSW_SYMDBGOFF]
: BLOCK [, BYTE],
```



```
917 3274 EXESECNAM = UPLIT BYTE (%ASCIC 'DST'),
918 3275 GSTSECNAM = UPLIT BYTE (%ASCIC 'GST');
919 3276
920 3277 LITERAL
921 3278 GL_OVERHEAD_REC = 2;
922 3279 SYMS_PER_GLREC = 28;
923 3280 START_ADDRESS = 0;
924 3281 END_ADDRESS = 1;
925 3282
926 3283 LOCAL
927 3284 STATUS : BLOCK[%UPVAL, BYTE],
928 3285 GLOBAL_RECORD : BLOCK[A_PAGE, BYTE],
929 3286 EXESECNAM_DESC : VECTOR [2, LONG],
930 3287 EXEFILNAM_DESC : VECTOR [2, LONG],
931 3288 GL_SYM_COUNT : VOLATILE;
932 3289
933 3290
934 3291 ++
935 3292 Check if this .EXE file has symbols at all. There are two kinds of checks
936 3293 which we make. First, we see if the image header is consistent.
937 3294 There are two checks for this - one which is always relevant, and one which
938 3295 is relevant only if we have already determined that there will be DSTs.
939 3296 --
940 3297 IF (.PAT$GL_IMGHDR [IHDSW_SYMDBGOFF] EQL 0)
941 3298 THEN
942 3299 BEGIN
943 3300 GST_BEGIN_ADDR = 0;
944 3301 DST_BEGIN_ADDR = 0;
945 3302 PAT$GB_SYMBOLS = FALSE;
946 3303 SIGNAL(PAT$NOGBL+MSG$K_INFO);
947 3304 SIGNAL(PAT$NOLCL+MSG$K_INFO);
948 3305 RETURN;
949 3306 END
950 3307 ELSE
951 3308 PAT$GB_SYMBOLS = TRUE;
952 3309
953 3310 ++
954 3311 Then we see if this is a simple case of there legitimately not being a DST.
955 3312 (i.e. the modules were simply not compiled with /DEBUG on).
956 3313 --
957 3314 IF ((DST_BEGIN_ADDR = .SYM_TBL_DATA[IHSSL_DSTVBN]) EQL 0)
958 3315 THEN
959 3316 BEGIN
960 3317 ++
961 3318 Check that the VBN of the DST is also zero. If it is not,
962 3319 then the image header is contradictory. Therefore, inform the
963 3320 user and fix the header by setting the DST fields to zero.
964 3321 This should only be an informational message.
965 3322 --
966 3323 IF (.SYM_TBL_DATA[IHSSL_DSTVBN] NEQ 0)
967 3324 THEN
968 3325 SIGNAL(PAT$INVIMGHDR+MSG$K_INFO);
969 3326 SIGNAL(PAT$NOLCL+MSG$K_INFO);
970 3327 DST_BEGIN_ADDR = 0;
971 3328 SYM_TBL_DATA[IHSSL_DSTVBN] = 0;
972 3329 SYM_TBL_DATA[IHSSW_DSTBLKS] = 0;
973 3330 END
974 3331 ELSE
```

```

974 3331
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980 3337
981 3338
982 3339
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1009 3366
1010 3367
1011 3368
1012 3369
1013 3370
1014 3371
1015 3372
1016 3373
1017 3374
1018 3375
1019 3376
1020 3377
1021 3378
1022 3379
1023 3380
1024 3381
1025 3382
1026 3383
1027 3384
1028 3385
1029 3386
1030 3387

      ++
      Check that the VBN is legal.  If not, then this is an inconsistent
      header.  Inform the user that it is invalid and
      fix up the header, ignoring the symbols that might be there.
      --
      IF (.SYM_TBL_DATA[IHSSL_DSTVBN] LEQ 2) OR
      (.SYM_TBL_DATA[IHSSW_DSTBLKS] LSS 0)
      THEN
          BEGIN
              SIGNAL(PATS_INVIMGHDR+MSG$K_INFO);
              SIGNAL(PATS_NOCLCL+MSG$K_INFO);
              DST_BEGIN_ADDR = 0;
              SYM_TBL_DATA[IHSSL_DSTVBN] = 0;
              SYM_TBL_DATA[IHSSW_DSTBLKS] = 0;
          END;

      ++
      Check that a GST exists.  If not, set an indicator.  Also make a valid image
      header.  This insures PAT$WRTIMG will work correctly.
      --
      IF ((GST_BEGIN_ADDR = .SYM_TBL_DATA[IHSSW_GSTRECS]) EQL 0)
      THEN
          BEGIN
              ++
              Check that the VBN of the GST is also zero.  If it is not,
              then the image header is contradictory.  Therefore, inform the
              user and fix the header by setting the GST fields to zero.
              This should only be an informational message.
              --
              IF (.SYM_TBL_DATA[IHSSL_GSTVBN] NEQ 0)
              THEN
                  SIGNAL(PATS_INVIMGHDR+MSG$K_INFO);
                  SIGNAL(PATS_NOGBL+MSG$K_INFO);
                  GST_BEGIN_ADDR = 0;
                  SYM_TBL_DATA[IHSSL_GSTVBN] = 0;
                  SYM_TBL_DATA[IHSSW_GSTRECS] = 0;
              END
          ELSE
              ++
              Check that the VBN is legal.  If not, then this is an inconsistent
              header.  Inform the user that it is invalid and
              fix up the header, ignoring the symbols that might be there.
              --
              IF (.SYM_TBL_DATA[IHSSL_GSTVBN] LEQ 2) OR
              (.SYM_TBL_DATA[IHSSW_GSTRECS] LSS 0)
              THEN
                  BEGIN
                      SIGNAL(PATS_INVIMGHDR+MSG$K_INFO);
                      SIGNAL(PATS_NOGBL+MSG$K_INFO);
                      GST_BEGIN_ADDR = 0;
                      SYM_TBL_DATA[IHSSL_GSTVBN] = 0;
                      SYM_TBL_DATA[IHSSW_GSTRECS] = 0;
                  END;

              ++
              Don't try to create and map the DST if there is not one in the .EXE file to map in.
              --

```

```
1031 3388 3 IF (.DST_BEGIN_ADDR NEQ 0)
1032 3389 3 THEN
1033 3390 3 BEGIN
1034 3391 3 PAT$GL_ISVADDR [START_ADDRESS] = 200; ! Set the address vectors to point to the
1035 3392 3 PAT$GL_ISVADDR [END_ADDRESS] = 200; ! first available addresses in P0 space.
1036 3393 3 EXESECNAM_DESC [0] = 3;
1037 3394 3 EXESECNAM_DESC [1] = EXESECNAM;
1038 3395 3 EXEFILNAM_DESC [0] = .PAT$GL_OLDNBK[NAM$B_RSL];
1039 3396 3 EXEFILNAM_DESC [1] = PAT$GB_OLDNAME;
1040 3397 3
1041 3398 3 IF NOT (STATUS = LIB$_CREMAPSEC (PAT$GL_ISVADDR
1042 3399 3 , PAT$GL_ISVADDR
1043 3400 3 , SEC$M_EXPREG
1044 3401 3 , EXESECNAM_DESC
1045 3402 3 , 0
1046 3403 3 , EXEFILNAM_DESC
1047 3404 3 , .SYM_TBL_DATA [IHSSW_DSTBLKS]
1048 3405 3 , .SYM_TBL_DATA [IHSSL_DSTVBN]))
1049 3406 3 THEN
1050 3407 3 BEGIN
1051 3408 3 ++
1052 3409 3 | Unconditionally make the severity level informational so
1053 3410 3 | that the message will be produced with no side effects.
1054 3411 3 --
1055 3412 3 ! STATUS[STSSV_SEVERITY] = SYS$K_INFO;
1056 3413 3 STATUS[STSSV_SEVERITY] = 3;
1057 3414 3 DST_BEGIN_ADDR = 0;
1058 3415 3 SIGNAL(PAT$ SYSERROR-MSG$K_FATAL+MSG$K_INFO, 0, .STATUS);
1059 3416 3 SIGNAL(.STATUS);
1060 3417 3 END
1061 3418 3 ELSE
1062 3419 3 ++
1063 3420 3 | Now load up the addresses of the beginning
1064 3421 3 | and end of the DST.
1065 3422 3 --
1066 3423 3 BEGIN
1067 3424 3 DST_BEGIN_ADDR = .PAT$GL_ISVADDR [START_ADDRESS];
1068 3425 3 DST_END_ADDR = .PAT$GL_ISVADDR [END_ADDRESS];
1069 3426 3 DST_NEXT_ADDR = .DST_BEGIN_ADDR;
1070 3427 3 END;
1071 3428 3 END; ! For no DSTs.
1072 3429 3
1073 3430 3 ++
1074 3431 3 | Now map in the GST in the same way we did the DST. Don't try to create and
1075 3432 3 | map the GST if there is not one in the .exe file to map in.
1076 3433 3 --
1077 3434 3 IF (.GST_BEGIN_ADDR NEQ 0)
1078 3435 3 THEN
1079 3436 3 BEGIN
1080 3437 3 LOCAL
1081 3438 3 GST_REC_PTR : REF VECTOR[WORD];
1082 3439 3
1083 3440 3 ++
1084 3441 3 | Find the last mapped address used and compute the addresses into
1085 3442 3 | which the GST will be mapped.
1086 3443 3 --
1087 3444 3 PAT$GL_ISVADDR[START_ADDRESS] = 200; ! Set the address vectors to point to the
```


1088 3445 3
1089 3446 3
1090 3447 3
1091 3448 3
1092 3449 3
1093 3450 3
1094 3451 4
1095 3452 4
1096 3453 4
1097 3454 4
1098 3455 4
1099 3456 4
1100 3457 4
1101 3458 4
1102 3459 4
1103 3460 3
1104 3461 4
1105 3462 4
1106 3463 4
1107 3464 4
1108 3465 4
1109 3466 4
1110 3467 4
1111 3468 4
1112 3469 4
1113 3470 4
1114 3471 4
1115 3472 4
1116 3473 3
1117 3474 4
1118 3475 4
1119 3476 4
1120 3477 4
1121 3478 4
1122 3479 4
1123 3480 4
1124 3481 4
1125 3482 4
1126 3483 4
1127 3484 4
1128 3485 4
1129 3486 4
1130 3487 4
1131 3488 4
1132 3489 4
1133 3490 4
1134 3491 4
1135 3492 4
1136 3493 4
1137 3494 4
1138 3495 4
1139 3496 4
1140 3497 4
1141 3498 4
1142 3499 4
1143 3500 4
1144 3501 4

```
PAT$GL_ISVADDR[END_ADDRESS] = 200;
EXESECNAM_DESC [0] = 3;
EXESECNAM_DESC [1] = GSTSECNAM;
EXEFILNAM_DESC [0] = .PAT$GL_OLDNBK[NAM$B_RSL];
EXEFILNAM_DESC [1] = PAT$GB_OLDNAME;

IF NOT (STATUS = LIB$_CREMAPSEC (PAT$GL_ISVADDR
                                , PAT$GL_ISVADDR
                                , SEC$M_EXPREG
                                , EXESECNAM_DESC
                                , 0
                                , EXEFILNAM_DESC
                                , .SYM_TBL_DATA [IH$SW_GSTRECS]
                                , .SYM_TBL_DATA [IH$SL_GSTVBN]
                                ))
THEN
    BEGIN
        ++
        Unconditionally make the severity level informational so
        that the message will be produced with no side effects.
        --
        STATUS[ST$SV_SEVERITY] = SYS$K_INFO;
        STATUS[ST$SV_SEVERITY] = 3;
        GST_BEGIN_ADDR = 0;
        GSR_BEGIN_ADDR = 0;
        SIGNAL (PAT$SYSE$ROR-MSG$K_FATAL+MSG$K_INFO, 0, .STATUS);
        SIGNAL(.STATUS);
    END
ELSE
    BEGIN
        ++
        Now skip the first two records because they
        are module header and module sub-header, respectively.
        NOTE: this builds in the knowledge of how these
        usually-RMS records are formatted.
        --
        GST_REC_PTR = .PAT$GL_ISVADDR[START_ADDRESS];

        ++
        Get to the next record by adding the rounded-up
        record byte count to the previous beginning
        virtual address, then adding on 2 because the count
        field is 2 bytes long.
        --
        GST_REC_PTR = .GST_REC_PTR + 2 + ((.GST_REC_PTR[0] + 1)/2)*2;

        ++
        Now skip the sub-module header.
        --
        GST_REC_PTR = .GST_REC_PTR + 2 + ((.GST_REC_PTR[0] + 1)/2)*2;

        ++
        And this is the address we wanted. Both the first, and, at this
        point, the 'next' records, start at this address.
        --
        GSR_BEGIN_ADDR = .GST_REC_PTR;
        GSR_NEXT_ADDR = .GSR_BEGIN_ADDR;
```

! first available addresses in P0 space.

PATINT
V04-000

J 1
16-Sep-1984 01:02:56
14-Sep-1984 12:52:34

VAX-11 Bliss-32 V4.0-742
DISK\$VMSMASTER:[PATCH.SRC]PATINT.B32;1 (7) Page 30

1145 3502 4
1146 3503 4
1147 3504 4
1148 3505 4
1149 3506 4
1150 3507 4
1151 3508 4
1152 3509 4
1153 3510 3
1154 3511 2
1155 3512 2
1156 3513 2
1157 3514 1

++
Tell the inner mechanism how many GST records there will be.
This number is the number that the LINKer gave us, -3,
because of the 2 records we just skipped over, PLUS the
module-end record at the end of the GST.

POSITION_GST(.SYM_TBL_DATA[IH\$W_GSTRECS] - 3);
END;

END;

! For no GSTs.

PAT\$INIT_RST (.GL_SYM_COUNT);
END;

.PSECT _PAT\$PLIT,NOWRT,NOEXE,0

54 53 44 03 00000 P.AAA: .ASCII <3>\DST\
54 53 47 03 00004 P.AAB: .ASCII <3>\GST\
:

EXESECNAM= P.AAA
GSTSECNAM= P.AAB

.PSECT _PAT\$CODE,NOWRT,2

OFFC 00000 .ENTRY PAT\$FIND_DST, Save R2,R3,R4,R5,R6,R7,R8,R9,-; 3217
R10,R11
5B 00000000G EF 9E 00002 MOVAB LIB\$CREMAPSEC, R11
5A 00000000G EF 9E 00009 MOVAB PAT\$GB_OLDNAME, R10
59 00000000G EF 9E 00010 MOVAB PAT\$GL_OLDNBK+3, R9
58 00000000G EF 9E 00017 MOVAB PAT\$GB_SYMBOLS, R8
57 00000000G EF 9E 0001E MOVAB PAT\$GL_ISVADDR, R7
56 00000000G 00 9E 00025 MOVAB LIB\$SIGNAL, R6
55 00000000' EF 9E 0002C MOVAB DST_BEGIN_ADDR, R5
5E FDEC CE 9E 00033 MOVAB -532(SP), SP
50 00000000G EF D0 00038 MOVL PAT\$GL_IMGHDR, R0
51 04 A0 3C 0003F MOVZWL 4(R0), R1
51 50 C1 00043 ADDL3 R0, R1, R2
04 A0 B5 00047 TSTW 4(R0)
1A 12 0004A BNEQ 1\$ 3296
14 A5 D4 0004C CLRL GST_BEGIN_ADDR 3299
65 D4 0004F CLRL DST_BEGIN_ADDR 3300
68 D4 00051 CLRL PAT\$GB_SYMBOLS 3301
006D81D3 8F DD 00053 PUSHL #7176659 3302
66 01 FB 00059 CALLS #1, LIB\$SIGNAL
006D81CB 8F DD 0005C PUSHL #7176651 3303
66 01 FB 00062 CALLS #1, LIB\$SIGNAL
04 00065 RET 3298
68 01 D0 00066 1\$: MOVL #1, PAT\$GB_SYMBOLS 3307
65 08 A2 3C 00069 MOVZWL 8(R2), DST_BEGIN_ADDR 3313
06 12 0006D BNEQ 2\$
62 D5 0006F TSTL (R2) 3322
07 12 00071 BNEQ 3\$
0E 11 00073 BRB 4\$ 3325
02 62 D1 00075 2\$: CMPL (R2), #2 3336

			19	14	00078	BGTR	5\$		
	006D8243		8F	DD	0007A	PUSHL	#7176771	3340	
66			01	FB	00080	CALLS	#1, LIB\$SIGNAL		
	006D81CB		8F	DD	00083	PUSHL	#7176651	3341	
66			01	FB	00089	CALLS	#1, LIB\$SIGNAL		
			65	D4	0008C	CLRL	DST_BEGIN_ADDR	3342	
			62	D4	0008E	CLRL	(R2)	3343	
	08		A2	B4	00090	CLRW	8(R2)	3344	
14	A5	0A	A2	3C	00093	MOVZWL	10(R2), GST_BEGIN_ADDR	3351	
			0A	12	00098	BNEQ	6\$		
54	04		A2	9E	0009A	MOVAB	4(R2), R4	3360	
			64	D5	0009E	TSTL	(R4)		
			0B	12	000A0	BNEQ	7\$		
			12	11	000A2	BRB	8\$	3363	
54	04		A2	9E	000A4	MOVAB	4(R2), R4	3374	
02			64	D1	000A8	CMPL	(R4), #2		
			1A	14	000AB	BGTR	9\$		
	006D8243		8F	DD	000AD	PUSHL	#7176771	3378	
66			01	FB	000B3	CALLS	#1, LIB\$SIGNAL		
	006D81D3		8F	DD	000B6	PUSHL	#7176659	3379	
66			01	FB	000BC	CALLS	#1, LIB\$SIGNAL		
	14		A5	D4	000BF	CLRL	GST_BEGIN_ADDR	3380	
			64	D4	000C2	CLRL	(R4)	3381	
	0A		A2	B4	000C4	CLRW	10(R2)	3382	
			65	D5	000C7	TSTL	DST_BEGIN_ADDR	3388	
			60	13	000C9	BEQL	11\$		
	67	C8	8F	9A	000CB	MOVZBL	#200, PAT\$GL_ISVADDR	3391	
04	A7	C8	8F	9A	000CF	MOVZBL	#200, PAT\$GL_ISVADDR+4	3392	
0C	AE		03	D0	000D4	MOVL	#3, EXESECNAM_DESC	3393	
10	AE	00000000	EF	9E	000D8	MOVAB	EXESECNAM, EXESECNAM_DESC+4	3394	
04	AE		69	9A	000E0	MOVZBL	PAT\$GL_OLDNBK+3, EXEFILNAM_DESC	3395	
08	AE		6A	9E	000E4	MOVAB	PAT\$GB_OLDNAME, EXEFILNAM_DESC+4	3396	
			62	DD	000E8	PUSHL	(R2)	3405	
	7E	08	A2	3C	000EA	MOVZWL	8(R2), -(SP)	3404	
		0C	AE	9F	000EE	PUSHAB	EXEFILNAM_DESC	3398	
			7E	D4	000F1	CLRL	-(SP)		
		1C	AE	9F	000F3	PUSHAB	EXESECNAM_DESC		
	00020000		8F	DD	000F6	PUSHL	#131072		
			57	DD	000FC	PUSHL	R7		
			57	DD	000FE	PUSHL	R7		
68			08	FB	00100	CALLS	#8, LIB\$CREMAPSEC		
53			50	D0	00103	MOVL	R0, STATUS		
18			53	E8	00106	BLBS	STATUS, 10\$		
53			03	F0	00109	INSV	#3, #0, #3, STATUS	3413	
00			65	D4	0010E	CLRL	DST_BEGIN_ADDR	3414	
			53	DD	00110	PUSHL	STATUS	3415	
			7E	D4	00112	CLRL	-(SP)		
	00000000G		8F	DD	00114	PUSHL	#PAT\$SYSERROR-1		
66			03	FB	0011A	CALLS	#3, LIB\$SIGNAL		
			53	DD	0011D	PUSHL	STATUS	3416	
66			01	FB	0011F	CALLS	#1, LIB\$SIGNAL		
			07	11	00122	BRB	11\$	3398	
65			67	7D	00124	MOVQ	PAT\$GL_ISVADDR, DST_BEGIN_ADDR	3424	
08	A5		65	D0	00127	MOVL	DST_BEGIN_ADDR, DST_NEXT_ADDR	3426	
	14		A5	D5	0012B	TSTL	GST_BEGIN_ADDR	3434	
			5B	13	0012E	BEQL	12\$		
67	C8		8F	9A	00130	MOVZBL	#200, PAT\$GL_ISVADDR	3444	

04	A7	C8	8F	9A	00134	MOVZBL	#200, PAT\$GL ISVADDR+4	3445	
0C	AE		03	D0	00139	MOVL	#3, EXESECNAM_DESC	3446	
10	AE	00000000'	EF	9E	0013D	MOVAB	GST\$ECNAM, EXESECNAM_DESC+4	3447	
04	AE		69	9A	00145	MOVZBL	PAT\$GL_OLDNBK+3, EXEFILNAM_DESC	3448	
08	AE		6A	9E	00149	MOVAB	PAT\$GB_OLDNAME, EXEFILNAM_DESC+4	3449	
			64	DD	0014D	PUSHL	(R4)	3458	
	7E	0A	A2	3C	0014F	MOVZWL	10(R2), -(SP)	3457	
		0C	AE	9F	00153	PUSHAB	EXEFILNAM_DESC	3451	
			7E	D4	00156	CLRL	-(SP)		
		1C	AE	9F	00158	PUSHAB	EXESECNAM_DESC		
		00020000	8F	DD	0015B	PUSHL	#131072		
			57	DD	00161	PUSHL	R7		
			57	DD	00163	PUSHL	R7		
	6B		08	FB	00165	CALLS	#8, LIB\$ CREMAPSEC		
	53		50	D0	00168	MOVL	R0, STATUS		
	1F		53	E8	0016B	BLBS	STATUS, 13\$		
53	00		03	F0	0016E	INSV	#3, #0, #3, STATUS	3467	
		14	A5	D4	00173	CLRL	GST_BEGIN_ADDR	3468	
		0C	A5	D4	00176	CLRL	GSR_BEGIN_ADDR	3469	
			53	DD	00179	PUSHL	STATUS	3470	
			7E	D4	0017B	CLRL	-(SP)		
		00000000G	8F	DD	0017D	PUSHL	#PAT\$ SYSERROR-1		
	66		03	FB	00183	CALLS	#3, LIB\$ SIGNAL		
			53	DD	00186	PUSHL	STATUS	3471	
	66		01	FB	00188	CALLS	#1, LIB\$ SIGNAL		
			34	11	0018B	BRB	14\$	3451	
	51		67	D0	0018D	MOVL	PAT\$GL ISVADDR, GST_REC_PTR	3481	
	50		61	3C	00190	MOVZWL	(GST_REC_PTR), R0	3489	
			50	D6	00193	INCL	R0		
	50		02	C6	00195	DIVL2	#2, R0		
	51	02	A140	3E	00198	MOVAB	2(GST_REC_PTR)[R0], GST_REC_PTR		
	50		61	3C	0019D	MOVZWL	(GST_REC_PTR), R0	3494	
			50	D6	001A0	INCL	R0		
	50		02	C6	001A2	DIVL2	#2, R0		
	51	02	A140	3E	001A5	MOVAB	2(GST_REC_PTR)[R0], GST_REC_PTR		
	OC		51	D0	001AA	MOVL	GST_REC_PTR, GSR_BEGIN_ADDR	3500	
	10		A5	D0	001AE	MOVL	GSR_BEGIN_ADDR, GSR_NEXT_ADDR	3501	
			7E	A2	001B3	MOVZWL	10(R2), -(SP)	3509	
			6E	C2	001B7	SUBL2	#3, (SP)		
	00000000V		EF	01	FB	001BA	CALLS	#1, POSITION_GST	
				6E	DD	001C1	PUSHL	GL_SYM_COUNT	3513
	00000000G		EF	01	FB	001C3	CALLS	#1, PAT\$INIT_RST	
				04	001CA	RET		3514	

; Routine Size: 459 bytes, Routine Base: _PAT\$CODE + 0286

```
1159 3515 1 GLOBAL ROUTINE PAT$GET_DST_REC ( REC_ID ) =
1160 3516 1
1161 3517 1 ++
1162 3518 1 FUNCTIONAL DESCRIPTION:
1163 3519 1
1164 3520 1     Make the indicated DST record available.
1165 3521 1
1166 3522 1 FORMAL PARAMETERS:
1167 3523 1
1168 3524 1     REC_ID - The ID of the record we are to fetch.
1169 3525 1             This ID must be one which was previously returned
1170 3526 1             by a call to PAT$GET_NXT_DST.
1171 3527 1
1172 3528 1 IMPLICIT INPUTS:
1173 3529 1
1174 3530 1     NONE
1175 3531 1
1176 3532 1 IMPLICIT OUTPUTS:
1177 3533 1
1178 3534 1     NONE
1179 3535 1
1180 3536 1 COMPLETION CODES:
1181 3537 1
1182 3538 1     0, if the indicated record does not exist,
1183 3539 1     the address of where it can now be referenced, otherwise.
1184 3540 1
1185 3541 1 SIDE EFFECTS:
1186 3542 1
1187 3543 1     The DST record is made available.
1188 3544 1
1189 3545 1 --
1190 3546 1
1191 3547 2 BEGIN
1192 3548 2
1193 3549 2 BIND
1194 3550 2     DST_RECNO = .REC_ID : DST_RECORD;
1195 3551 2
1196 3552 2 ++
1197 3553 2 If there is no DST, simply return as though we were asked to read one
1198 3554 2 past the last one. (The interface's notion of EOF).
1199 3555 2 --
1200 3556 2 IF (.DST_BEGIN_ADDR EQL 0)
1201 3557 2 THEN
1202 3558 2     RETURN(0);
1203 3559 2
1204 3560 2 ++
1205 3561 2 The record ID is the same as the virtual address at which it can be
1206 3562 2 referenced. The next record, then, is simply the one which is virtually
1207 3563 2 contiguous to this one, excepting for the case of the last record.
1208 3564 2 Here we are lenient - we say that the DST ended OK if one asks for a
1209 3565 2 record which is past the end marker, OR, if the count field
1210 3566 2 for a supposed 'next' record is 0.
1211 3567 2 --
1212 3568 2 IF (.REC_ID EQL .DST_END_ADDR +1)
1213 3569 2 THEN
1214 3570 2     RETURN(0);
1215 3571 2
```

```
1216 3572 2 1++
1217 3573 2 1-- Now that it is safe, check for 0-length records.
1218 3574 2 1--
1219 3575 2 1IF (.DST_REC RD [DSTR_SIZE] EQL 0)
1220 3576 2 1THEN
1221 3577 2 1    RETURN(0);
1222 3578 2 1
1223 3579 2 1++
1224 3580 2 1-- Then check that the ID is valid.
1225 3581 2 1--
1226 3582 2 1IF (.REC_ID LSSA .dst_begin_addr) OR (.REC_ID GTRA .dst_end_addr)
1227 3583 2 1THEN
1228 3584 2 1    BEGIN
1229 3585 2 1    1++
1230 3586 2 1    1-- This should not happen - we check and report
1231 3587 2 1    1-- errors here only to help us while debugging.
1232 3588 2 1    1--
1233 3589 2 1    SIGNAL (PAT$_INV DSTREC); ! Severe error
1234 3590 2 1    RETURN(0);
1235 3591 2 1    END;
1236 3592 2 1
1237 3593 2 1RETURN( .REC_ID );
1238 3594 2 1END;
```

			000C 00000	.ENTRY	PAT\$GET_DST_REC, Save R2,R3	3515
	53	00000000	EF 9E 00002	MOVAB	DST_END_ADDR, R3	
	52	04	AC D0 00009	MOVL	REC_ID, R2	3550
	51	FC	A3 D0 0000D	MOVL	DST_BEGIN_ADDR, R1	3556
			2A 13 00011	BEQL	3\$	
50	63		01 C1 00013	ADDL3	#1, DST_END_ADDR, R0	3568
	50		52 D1 00017	CMPL	R2, R0	
			21 13 0001A	BEQL	3\$	
			62 95 0001C	TSTB	(R2)	3575
			1D 13 0001E	BEQL	3\$	
	51		52 D1 00020	CMPL	R2, R1	3582
			05 1F 00023	BLSSU	1\$	
	63		52 D1 00025	CMPL	R2, DST_END_ADDR	
			0F 1B 00028	BLEQU	2\$	
		006D80E2	8F DD 0002A	PUSHL	#7176418	3589
	00000000G	00	01 FB 00030	CALLS	#1, LIB\$SIGNAL	
			04 11 00037	BRB	3\$	3590
	50		52 D0 00039	MOVL	R2, R0	3593
			04 0003C	RET		
			50 D4 0003D	CLRL	R0	3594
			04 0003F	RET		

; Routine Size: 64 bytes, Routine Base: _PAT\$CODE + 0451

```
1240 3595 1 GLOBAL ROUTINE PAT$POSITON_DST ( REC_ID ) =
1241 3596 1
1242 3597 1 ++
1243 3598 1 FUNCTIONAL DESCRIPTION:
1244 3599 1
1245 3600 1     Make the indicated DST record available in such
1246 3601 1     a way that PAT$GET_NXT_DST's idea of 'next' is
1247 3602 1     defined to be the one after this routine fetches.
1248 3603 1
1249 3604 1 FORMAL PARAMETERS:
1250 3605 1
1251 3606 1     REC_ID - The ID of the record we are to fetch.
1252 3607 1     This ID must be one which was previously returned
1253 3608 1     by a call to PAT$GET_NXT_DST.
1254 3609 1
1255 3610 1 IMPLICIT INPUTS:
1256 3611 1
1257 3612 1     NONE
1258 3613 1
1259 3614 1 IMPLICIT OUTPUTS:
1260 3615 1
1261 3616 1     NONE
1262 3617 1
1263 3618 1 COMPLETION CODES:
1264 3619 1
1265 3620 1     0, if the indicated record does not exist,
1266 3621 1     the address of where it can now be referenced, otherwise.
1267 3622 1
1268 3623 1 SIDE EFFECTS:
1269 3624 1
1270 3625 1     The DST record is made available.
1271 3626 1     The 'next' DST record is henceforth defined to
1272 3627 1     be the one after the one fetched by this call.
1273 3628 1
1274 3629 1 --
1275 3630 1
1276 3631 2 BEGIN
1277 3632 2
1278 3633 2 LOCAL
1279 3634 2     REC_ADDR : REF DST_RECORD;
1280 3635 2
1281 3636 2 ++
1282 3637 2 PAT$GET_DST_REC does most of the work -
1283 3638 2 we just include the above-described side effect.
1284 3639 2 --
1285 3640 2 IF ((REC_ADDR = PAT$GET_DST_REC( .REC_ID )) EQL 0 )
1286 3641 2 THEN
1287 3642 2     RETURN(0);
1288 3643 2
1289 3644 2 ++
1290 3645 2 RE-initialize INT's notion of 'next' DST record.
1291 3646 2 --
1292 3647 2 DST_NEXT_ADDR = .REC_ADDR + .REC_ADDR [DSTR_SIZE] +1;
1293 3648 2 RETURN( .REC_ADDR );
1294 3649 1 END;
```


			0000 00000	.ENTRY	PAT\$POSITION_DST, Save nothing	:	3595
		04	AC DD 00002	PUSHL	REC_ID	:	3640
B7	AF		01 FB 00005	CALLS	#1, PAT\$GET_DST_REC	:	
			50 D5 00009	TSTL	REC_ADDR	:	
			0D 13 0000B	BEQL	1\$:	
	51		60 9A 0000D	MOVZBL	(REC_ADDR), R1	:	3647
00000000'	EF	01	A140 9E 00010	MOVAB	1(R1)[REC_ADDR], DST_NEXT_ADDR	:	
			04 00019	RET		:	3648
		50	D4 0001A 1\$:	CLRL	R0	:	3649
			04 0001C	RET		:	

; Routine Size: 29 bytes, Routine Base: _PAT\$CODE + 0491

```
1296 3650 1 ROUTINE POSITION_GST ( GST_REC_COUNT ) =
1297 3651 1
1298 3652 1 ++
1299 3653 1 FUNCTIONAL DESCRIPTION:
1300 3654 1
1301 3655 1 This routine, if called with a positive value initializes its OWN
1302 3656 1 storage to remember the number of RMS-type records in the GST.
1303 3657 1 If it is called with a negative or zero value, it returns the address
1304 3658 1 of the next RMS-type record in the GST. A negative value also causes
1305 3659 1 the pointers to be positioned at the start of the GST.
1306 3660 1
1307 3661 1 FORMAL PARAMETERS:
1308 3662 1
1309 3663 1 GST_REC_COUNT - The number of RMS records in the GST.
1310 3664 1 (negative value) re-position to start and return
1311 3665 1 address of first GLOBAL.
1312 3666 1 (zero) return address of the next GLOBAL.
1313 3667 1
1314 3668 1 IMPLICIT INPUTS:
1315 3669 1
1316 3670 1 GSR_BEGIN_ADDR - Holds the starting address of the GST.
1317 3671 1 If the value is not GTR 0 or 1, then the GST
1318 3672 1 has not been mapped in so this routine returns 0.
1319 3673 1
1320 3674 1 IMPLICIT OUTPUTS:
1321 3675 1
1322 3676 1 GSR_NEXT_ADDR - Holds the address of the next RMS record in the GST
1323 3677 1 or the GST was not mapped in.
1324 3678 1
1325 3679 1 ROUTINE VALUE:
1326 3680 1
1327 3681 1 0 - If there are no more records in the GST.
1328 3682 1 non-zero - The address of the next GST RMS record.
1329 3683 1
1330 3684 1 SIDE EFFECTS:
1331 3685 1
1332 3686 1 The next GST record can now be accessed, and an OWN pointer to the next
1333 3687 1 one is maintained. The number of GST records yet to go is also updated
1334 3688 1 so that the end of the GST can be detected.
1335 3689 1
1336 3690 1 --
1337 3691 1
1338 3692 2 BEGIN
1339 3693 2
1340 3694 2 OWN
1341 3695 2 TOTAL_RECORDS,
1342 3696 2 RECORDS_LEFT;
1343 3697 2
1344 3698 2 LOCAL
1345 3699 2 BLOCK_ADDR;
1346 3700 2
1347 3701 2 ++
1348 3702 2 If there is no mapped GST, then return 0, no matter why this routine
1349 3703 2 was called.
1350 3704 2 --
1351 3705 2 IF (NOT .GSR_BEGIN_ADDR GTRA 1)
1352 3706 2 THEN
```

```
1353 3707 2      RETURN(0);
1354 3708 2
1355 3709 2      IF (.GST_REC_COUNT GTR 0)
1356 3710 2      THEN
1357 3711 2          BEGIN
1358 3712 2              TOTAL_RECORDS = .GST_REC_COUNT;
1359 3713 2              RETURN (0);
1360 3714 2          END;
1361 3715 2
1362 3716 2      IF (.GST_REC_COUNT NEQ 0)
1363 3717 2      THEN
1364 3718 2          BEGIN
1365 3719 2              GSR_NEXT_ADDR = .GSR_BEGIN_ADDR;
1366 3720 2              RECORDS_LEFT = .TOTAL_RECORDS;
1367 3721 2          END;
1368 3722 2
1369 3723 2      ++
1370 3724 2      Stop the following from faulting if some caller ignores the end condition and
1371 3725 2      effectively causes us to 'run off the end' of the mapped GST.
1372 3726 2      --
1373 3727 2      IF (NOT .RECORDS_LEFT GEQ 1)
1374 3728 2      THEN
1375 3729 2          RETURN(0);
1376 3730 2
1377 3731 2      ++
1378 3732 2      Pick up the address of the current record, and update the pointer to the
1379 3733 2      subsequent one.
1380 3734 2      --
1381 3735 2      BLOCK_ADDR = .GSR_NEXT_ADDR + 2;
1382 3736 2      GSR_NEXT_ADDR = .GSR_NEXT_ADDR + 2 + ((.GSR_NEXT_ADDR[0] + 1)/2)*2;
1383 3737 2      RECORDS_LEFT = .RECORDS_LEFT - 1;
1384 3738 2      RETURN (.BLOCK_ADDR);
1385 3739 1      END;
```

.PSECT _PAT\$OWN,NOEXE,2

00024 TOTAL_RECORDS:
 .BLK 4
00028 RECORDS_LEFT:
 .BLK 4

.PSECT _PAT\$CODE,NOWRT,2

000C 00000 POSITION				GST:			
53	00000000'	EF	9E	00002	WORD	Save R2,R3	3650
01	FC	A3	D1	00009	MOVAB	GSR_NEXT_ADDR, R3	
		34	1B	0000D	CMPL	GSR_BEGIN_ADDR, #1	3705
50	04	AC	D0	0000F	BLEQU	3\$	3709
		06	15	00013	MOVL	GST_REC_COUNT, R0	
10	A3	50	D0	00015	BLEQ	1\$	3712
		28	11	00019	MOVL	R0, TOTAL_RECORDS	3713
		09	13	0001B	BRB	3\$	
					BEQL	2\$	3716

PATINT
V04-000

F 2
16-Sep-1984 01:02:56
14-Sep-1984 12:52:34

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14	63	FC	A3	D0	0001D	MOVL	GSR BEGIN ADDR, GSR NEXT_ADDR	...	3719
	A3	10	A3	D0	00021	MOVL	TOTAL RECORDS, RECORDS_LEFT	...	3720
		14	A3	D5	00026	TSTL	RECORDS_LEFT	...	3727
			18	15	00029	BLEQ	3\$...	
	52		63	D0	0002B	MOVL	GSR NEXT_ADDR, R2	...	3735
	50	02	A2	9E	0002E	MOVAB	2(R2), BLOCK_ADDR	...	
	51		62	3C	00032	MOVZWL	(R2), R1	...	3736
			51	D6	00035	INCL	R1	...	
	51		02	C6	00037	DIVL2	#2, R1	...	
	63	02	A241	3E	0003A	MOVAW	2(R2)[R1], GSR_NEXT_ADDR	...	
		14	A3	D7	0003F	DECL	RECORDS_LEFT	...	3737
				04	00042	RET		...	3738
			50	D4	00043	CLRL	R0	...	3739
				04	00045	RET		...	

; Routine Size: 70 bytes, Routine Base: _PAT\$CODE + 04AE


```
1387 3740 1 GLOBAL ROUTINE PAT$GET_NXT_DST ( REC_ID_PTR ) =
1388 3741 1
1389 3742 1 ++
1390 3743 1 FUNCTIONAL DESCRIPTION:
1391 3744 1
1392 3745 1     Make the next DST record available,
1393 3746 1     and return both a pointer to where it
1394 3747 1     can now be referenced, as well as an ID
1395 3748 1     for it so that we can ask for it later.
1396 3749 1
1397 3750 1 FORMAL PARAMETERS:
1398 3751 1
1399 3752 1     REC_ID_PTR - the address of where this routine will
1400 3753 1     stuff the ID it wants subsequent calls
1401 3754 1     to PAT$GET_DST_REC to use to refer
1402 3755 1     to the record fetched by this call.
1403 3756 1
1404 3757 1 IMPLICIT INPUTS:
1405 3758 1
1406 3759 1     To be defined.
1407 3760 1     (whatever context these routines work from).
1408 3761 1
1409 3762 1 IMPLICIT OUTPUTS:
1410 3763 1
1411 3764 1     none
1412 3765 1
1413 3766 1 COMPLETION CODES:
1414 3767 1
1415 3768 1     0, if the indicated record does not exist,
1416 3769 1     the address of where it can now be referenced, otherwise.
1417 3770 1
1418 3771 1 SIDE EFFECTS:
1419 3772 1
1420 3773 1     The DST record after the last one fetched is made available.
1421 3774 1     If no record has yet been fetched, the first record in
1422 3775 1     the DST is made available.
1423 3776 1
1424 3777 1 --
1425 3778 1
1426 3779 2 BEGIN
1427 3780 2
1428 3781 2 MAP
1429 3782 2     REC_ID_PTR : REF VECTOR[,LONG];
1430 3783 2
1431 3784 2 ++
1432 3785 2     Since for us record IDs are the same as their virtual addresses, we can get
1433 3786 2     the next one the same way we can get ANY one. The only detail to fill in is
1434 3787 2     passing back the ID for this next one.
1435 3788 2 --
1436 3789 2 RETURN(REC_ID_PTR[0] = PAT$POSITION_DST( .DST_NEXT_ADDR ));
1437 3790 1 END;
```

0000 00000

.ENTRY PAT\$GET_NXT_DST, Save nothing

; 3740

PATINT
V04-000

H 2
16-Sep-1984 01:02:56
14-Sep-1984 12:52:34

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DISK\$VMSMASTER:[PATCH.SRC]PATINT.B32;1 (11)

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91	AF	00000000'	EF	DD	00002	PUSHL	DST_NEXT_ADDR	:	3789
04	BC		01	FB	00008	CALLS	#1, _PAT\$POSITION_DST	:	
			50	D0	0000C	MOVL	R0, @REC_ID_PTR	:	
			04	00010		RET		:	3790

; Routine Size: 17 bytes, Routine Base: _PAT\$CODE + 04F4

```
1439 3791 1 GLOBAL ROUTINE PAT$GET_NXT_GST (ACCESS_FLAG) =
1440 3792 1
1441 3793 1 ++
1442 3794 1 Functional description:
1443 3795 1
1444 3796 1 This routine returns the address of a fixed length record that
1445 3797 1 contains a global symbol name and its associated value. This
1446 3798 1 routine expects to be called repeatedly until each global symbol
1447 3799 1 has been returned to the caller.
1448 3800 1
1449 3801 1 Before this routine is ever called, the location of the GST in
1450 3802 1 the image file is found, and it is mapped into PATCH's image.
1451 3803 1 The address of this buffer is held in the OWN variable GST_BEGIN_ADDR.
1452 3804 1 This routine analyzes the GST record, and moves through the buffer,
1453 3805 1 returning the buffer address of each global symbol entry as it is seen.
1454 3806 1 When the buffer is exhausted, this routine reads in the next GST record.
1455 3807 1 It halts at end of file and returns a value of zero to the caller.
1456 3808 1
1457 3809 1 This routine keeps the variable GST_BEGIN_ADDR up to date.
1458 3810 1
1459 3811 1 The format of one of these concatenated records is a single
1460 3812 1 leading byte containing the value 1, indicating that the record
1461 3813 1 is indeed a GSD record. The variable GST_BEGIN_ADDR addresses
1462 3814 1 the byte following this leading byte.
1463 3815 1
1464 3816 1 Each entry in the record has a fixed number of overhead
1465 3817 1 bytes followed by a symbol name that is a variable number of
1466 3818 1 bytes. The entries we are interested in processing are the
1467 3819 1 global symbol definitions, entry point symbol and mask
1468 3820 1 definitions, and procedure definitions with formal argument descriptions.
1469 3821 1 The other defined type, PSECT definition, is noted only because it
1470 3822 1 must be successfully passed over. The format of each of these types is
1471 3823 1 illustrated below:
1472 3824 1
1473 3825 1
1474 3826 1 Global symbol definition:
1475 3827 1
1476 3828 1
1477 3829 1 0 ! GSD type 1 !
1478 3830 1
1479 3831 1 1 ! data type ! ignored for now
1480 3832 1
1481 3833 1 2 ! flag !
1482 3834 1 3 ! bytes ! bit 1 set means that this is
1483 3835 1 4 ! psect index ! a definition. ignore bit 0.
1484 3836 1 5 ! value ! ignored.
1485 3837 1 6 ! 4 bytes
1486 3838 1
1487 3839 1 9 !
1488 3840 1 ! symbol !
1489 3841 1 ! name ! stock counted character
1490 3842 1 ! string.
1491 3843 1
1492 3844 1
1493 3845 1
1494 3846 1
1495 3847 1
```

1496 3848 1
1497 3849 1
1498 3850 1
1499 3851 1
1500 3852 1
1501 3853 1
1502 3854 1
1503 3855 1
1504 3856 1
1505 3857 1
1506 3858 1
1507 3859 1
1508 3860 1
1509 3861 1
1510 3862 1
1511 3863 1
1512 3864 1
1513 3865 1
1514 3866 1
1515 3867 1
1516 3868 1
1517 3869 1
1518 3870 1
1519 3871 1
1520 3872 1
1521 3873 1
1522 3874 1
1523 3875 1
1524 3876 1
1525 3877 1
1526 3878 1
1527 3879 1
1528 3880 1
1529 3881 1
1530 3882 1
1531 3883 1
1532 3884 1
1533 3885 1
1534 3886 1
1535 3887 1
1536 3888 1
1537 3889 1
1538 3890 1
1539 3891 1
1540 3892 1
1541 3893 1
1542 3894 1
1543 3895 1
1544 3896 1
1545 3897 1
1546 3898 1
1547 3899 1
1548 3900 1
1549 3901 1
1550 3902 1
1551 3903 1
1552 3904 1

The entry point symbol and mask definition entry is identical to the global symbol definition illustrated above, with the addition of a two byte field for the procedure's register save mask. This two byte field is located after the symbol value field (which is an entry point address).

0	! GSD type 2 !	
1	! data type !	ignored for now
2	! flag bytes !	not relevant for entry point def.
4	! psect index !	ignored
5	! value !	4 bytes
9	! register save mask !	ignored, 2 bytes
11	! symbol name !	stock counted character string

The procedure definition with formal argument descriptions is identical to the entry point with mask definition above, save that it has some additional fields. There is a minimum number of arguments byte and a maximum number of arguments byte. These are followed by a formal argument description for each possible argument (i.e., the maximum number). The formal argument descriptions consist of an argument value control byte and a remaining count byte. The remaining count byte tells the number of bytes in the detailed argument description (from 0 to 255).

0	! GSD type 3 !	
1	! data type !	ignored for now
2	! flag bytes !	bit 1 set means that this is a definition. ignore bit 0.
4	! psect index !	ignored
5	! value !	4 bytes
9	! register save mask !	ignored, 2 bytes
11	! symbol name !	

1553 3905 1
1554 3906 1
1555 3907 1
1556 3908 1
1557 3909 1
1558 3910 1
1559 3911 1
1560 3912 1
1561 3913 1
1562 3914 1
1563 3915 1
1564 3916 1
1565 3917 1
1566 3918 1
1567 3919 1
1568 3920 1
1569 3921 1
1570 3922 1
1571 3923 1
1572 3924 1
1573 3925 1
1574 3926 1
1575 3927 1
1576 3928 1
1577 3929 1
1578 3930 1
1579 3931 1
1580 3932 1
1581 3933 1
1582 3934 1
1583 3935 1
1584 3936 1
1585 3937 1
1586 3938 1
1587 3939 1
1588 3940 1
1589 3941 1
1590 3942 1
1591 3943 1
1592 3944 1
1593 3945 1
1594 3946 1
1595 3947 1
1596 3948 1
1597 3949 1
1598 3950 1
1599 3951 1
1600 3952 1
1601 3953 1
1602 3954 1
1603 3955 1
1604 3956 1
1605 3957 1
1606 3958 1
1607 3959 1
1608 3960 1
1609 3961 1

symbol name

! min # act arg !

! max # act arg !

! formal arg #1 description

!

! formal arg #n description

stock counted character
string

1 byte

1 byte

Each formal argument description has the following format:

0	! arg. val. ctl.!	1 byte
1	! rem. byte cnt.!	1 byte
	! detailed argument description	anywhere from 0-255 bytes

PSECT definition:

0	! GSD type 0 !	
1	! alignment	
2	! flag	
3	! bytes	
4	! allocation	4 bytes
8	! symbol name	stock counted character string.

PSECT definition in a Shareable Image:

0	! GSD type 0 !
---	----------------

Address	Offset	Field	Size
1610	3962	1	alignment
1611	3963	2	flag
1612	3964	3	bytes
1613	3965	4	allocation
1614	3966	8	base address within Shareable Image
1615	3967	12	symbol name
1616	3968		4 bytes
1617	3969		4 bytes
1618	3970		
1619	3971		
1620	3972		
1621	3973		
1622	3974		
1623	3975		
1624	3976		
1625	3977		
1626	3978		
1627	3979		
1628	3980		
1629	3981		
1630	3982		
1631	3983		
1632	3984		
1633	3985		
1634	3986		
1635	3987		
1636	3988		
1637	3989		
1638	3990		
1639	3991		
1640	3992		
1641	3993		
1642	3994		
1643	3995		
1644	3996		
1645	3997		
1646	3998		
1647	3999		
1648	4000		
1649	4001		
1650	4002		
1651	4003		
1652	4004		
1653	4005		
1654	4006		
1655	4007		
1656	4008		
1657	4009		
1658	4010		
1659	4011		
1660	4012		
1661	4013		
1662	4014		
1663	4015		
1664	4016		
1665	4017		
1666	4018		

Calling sequence:
CALLS #0, PAT\$GET_NXT_GST

Inputs:
none

Implicit inputs:
GST_BEGIN_ADDR - Current address of record buffer

Outputs:
The address of the next global symbol entry, or 0, if EOF.

Implicit outputs:
GST_BEGIN_ADDR is updated to address the next entry.

Routine value:
An address or 0

Side effects:
Another record may be read in.

--

BEGIN

LOCAL

OLD_ADDRESS : REF BLOCK [, BYTE];

LABEL

GET_RECORD;

IF (.ACCESS_FLAG NEQ 0)

```
1667 4019 2 THEN
1668 4020 3 BEGIN
1669 4021 4 IF ((GST_BEGIN_ADDR = POSITION_GST(-1)) EQL 0)
1670 4022 5 THEN
1671 4023 6 GST_BEGIN_ADDR = %X'FFFFFFFF';
1672 4024 7 RETURN(0);
1673 4025 8 END;
1674 4026 9
1675 4027 10 ++
1676 4028 11 See whether the current buffer address is beyond the
1677 4029 12 end of the last GST record we looked at. Note that we
1678 4030 13 rounded up GSR_NEXT_ADDR when calculating where the next
1679 4031 14 GST record will begin. Therefore we must temporarily round
1680 4032 15 it down again when comparing it with GST_BEGIN_ADDR since it
1681 4033 16 may point to the last unused byte in a GST record.
1682 4034 17 --
1683 4035 18 REPEAT
1684 4036 19 GET_RECORD:
1685 4037 20 BEGIN
1686 4038 21 ++
1687 4039 22 First check that there is a GST in this image.
1688 4040 23 --
1689 4041 24 IF (.GST_BEGIN_ADDR EQL 0)
1690 4042 25 THEN
1691 4043 26 RETURN(0);
1692 4044 27
1693 4045 28 IF (.GST_BEGIN_ADDR GEQA .GSR_NEXT_ADDR-1)
1694 4046 29 THEN
1695 4047 30 BEGIN
1696 4048 31 ++
1697 4049 32 Record was finished. Check that there are more records.
1698 4050 33 If so, then get another record.
1699 4051 34 --
1700 4052 35 IF ((GST_BEGIN_ADDR = POSITION_GST(0)) EQL 0)
1701 4053 36 THEN
1702 4054 37 RETURN(0)
1703 4055 38 ELSE
1704 4056 39 BEGIN
1705 4057 40 ++
1706 4058 41 If the next record is a GST record, then initialize
1707 4059 42 the variable GST_BEGIN_ADDR to point to the first
1708 4060 43 global symbol definition block in this record.
1709 4061 44 --
1710 4062 45 LOCAL
1711 4063 46 BUFFER_ADDRESS : REF VECTOR [, BYTE];
1712 4064 47
1713 4065 48 BUFFER_ADDRESS = .GST_BEGIN_ADDR;
1714 4066 49 IF .BUFFER_ADDRESS [GST_RECORD_TYPE] EQL GST_TYPE
1715 4067 50 THEN
1716 4068 51 GST_BEGIN_ADDR = .GST_BEGIN_ADDR + 1
1717 4069 52 ELSE
1718 4070 53 BEGIN
1719 4071 54 ++
1720 4072 55 This record is not a GST record.
1721 4073 56 Go on to the next.
1722 4074 57 --
1723 4075 58 GST_BEGIN_ADDR = %X'FFFFFFFF';
```

```

1724 4076 6      LEAVE GET_RECORD;
1725 4077 5      END;
1726 4078 4      END;
1727 4079 4      END;
1728 4080 3      ELSE
1729 4081 4      BEGIN
1730 4082 4      ++
1731 4083 4      | This is a global symbol. Save its address.
1732 4084 4      | Then update the variable GST_BEGIN_ADDR to
1733 4085 4      | point to the next symbol.
1734 4086 4      --
1735 4087 4      OLD_ADDRESS = .GST_BEGIN_ADDR;
1736 4088 4      CASE .OLD_ADDRESS [ENTRY_TYPE] FROM GSD$C_PSC TO GSD$C_SPSC OF
1737 4089 4
1738 4090 4      SET
1739 4091 4
1740 4092 4      [GSD$C_PSC]:
1741 4093 4      BEGIN
1742 4094 5      GST_BEGIN_ADDR = .OLD_ADDRESS +
1743 4095 6      (OLD_ADDRESS[GPS$T_NAME] - OLD_ADDRESS[GPS$T_START])
1744 4096 5      + .OLD_ADDRESS [GPS$B_NAMLANG];
1745 4097 4      END;
1746 4098 4
1747 4099 4      [GSD$C_SYM]:
1748 4100 4      BEGIN
1749 4101 5      GST_BEGIN_ADDR = .OLD_ADDRESS +
1750 4102 5      (OLD_ADDRESS[SDF$T_NAME] - OLD_ADDRESS[SDF$T_START])
1751 4103 6      + .OLD_ADDRESS [SDF$B_NAMLANG];
1752 4104 5      RETURN .OLD_ADDRESS
1753 4105 5      END;
1754 4106 4
1755 4107 4      [GSD$C_EPM]:
1756 4108 4      BEGIN
1757 4109 5      GST_BEGIN_ADDR = .OLD_ADDRESS +
1758 4110 5      (OLD_ADDRESS[EPM$T_NAME] - OLD_ADDRESS[EPM$T_START])
1759 4111 5      + .OLD_ADDRESS [EPM$B_NAMLANG];
1760 4112 6      RETURN .OLD_ADDRESS
1761 4113 5      END;
1762 4114 4
1763 4115 4      [GSD$C_PRO]:
1764 4116 4      BEGIN
1765 4117 4      LOCAL
1766 4118 4      NUM_ARGS;
1767 4119 5      ! Max formal args
1768 4120 5      GST_BEGIN_ADDR = .OLD_ADDRESS +
1769 4121 5      (OLD_ADDRESS[EPM$T_NAME] - OLD_ADDRESS[EPM$T_START])
1770 4122 5      + .OLD_ADDRESS [EPM$B_NAMLANG];
1771 4123 6      NUM_ARGS = .GST_BEGIN_ADDR[GST_P_MAX_ARG];
1772 4124 5      GST_BEGIN_ADDR = .GST_BEGIN_ADDR + MINMAX_OVERHEAD;
1773 4125 5      WHILE (.NUM_ARGS GTR 0)
1774 4126 5      DO
1775 4127 6      BEGIN
1776 4128 6      GST_BEGIN_ADDR = .GST_BEGIN_ADDR +
1777 4129 6      .GST_BEGIN_ADDR[GST_P_REM_CNT] + ARGDSC_OVERHEAD;
1778 4130 6      NUM_ARGS = .NUM_ARGS - 1;
1779 4131 6
1780 4132 6
```



```

END;
RETURN .OLD_ADDRESS
END;

[GSDD$C_SPSC]:
BEGIN
GST_BEGIN_ADDR = .OLD_ADDRESS +
                  (OLD_ADDRESS[SGPS$T_NAME] - OLD_ADDRESS[SGPS$T_START])
                  + .OLD_ADDRESS [SGPS$B_NAMLANG];
END;

[INRANGE]:
BEGIN
GST_BEGIN_ADDR = %X'FFFFFFFF';
END;

[OUTRANGE]:
BEGIN
GST_BEGIN_ADDR = %X'FFFFFFFF';
END;

TES;
```

PC	Op	OpC	OpD	OpI	OpR	OpS	OpT	OpV	OpW	OpX	OpY	OpZ	OpAA	OpAB	OpAC	OpAD	OpAE	OpAF	OpAG	OpAH	OpAI	OpAJ	OpAK	OpAL	OpAM	OpAN	OpAO	OpAP	OpAQ	OpAR	OpAS	OpAT	OpAU	OpAV	OpAW	OpAX	OpAY	OpAZ	OpBA	OpBB	OpBC	OpBD	OpBE	OpBF	OpBG	OpBH	OpBI	OpBJ	OpBK	OpBL	OpBM	OpBN	OpBO	OpBP	OpBQ	OpBR	OpBS	OpBT	OpBU	OpBV	OpBW	OpBX	OpBY	OpBZ	OpCA	OpCB	OpCC	OpCD	OpCE	OpCF	OpCG	OpCH	OpCI	OpCJ	OpCK	OpCL	OpCM	OpCN	OpCO	OpCP	OpCQ	OpCR	OpCS	OpCT	OpCU	OpCV	OpCW	OpCX	OpCY	OpCZ	OpDA	OpDB	OpDC	OpDD	OpDE	OpDF	OpDG	OpDH	OpDI	OpDJ	OpDK	OpDL	OpDM	OpDN	OpDO	OpDP	OpDQ	OpDR	OpDS	OpDT	OpDU	OpDV	OpDW	OpDX	OpDY	OpDZ	OpEA	OpEB	OpEC	OpED	OpEE	OpEF	OpEG	OpEH	OpEI	OpEJ	OpEK	OpEL	OpEM	OpEN	OpEO	OpEP	OpEQ	OpER	OpES	OpET	OpEU	OpEV	OpEW	OpEX	OpEY	OpEZ	OpFA	OpFB	OpFC	OpFD	OpFE	OpFF	OpFG	OpFH	OpFI	OpFJ	OpFK	OpFL	OpFM	OpFN	OpFO	OpFP	OpFQ	OpFR	OpFS	OpFT	OpFU	OpFV	OpFW	OpFX	OpFY	OpFZ	OpGA	OpGB	OpGC	OpGD	OpGE	OpGF	OpGG	OpGH	OpGI	OpGJ	OpGK	OpGL	OpGM	OpGN	OpGO	OpGP	OpGQ	OpGR	OpGS	OpGT	OpGU	OpGV	OpGW	OpGX	OpGY	OpGZ	OpHA	OpHB	OpHC	OpHD	OpHE	OpHF	OpHG	OpHH	OpHI	OpHJ	OpHK	OpHL	OpHM	OpHN	OpHO	OpHP	OpHQ	OpHR	OpHS	OpHT	OpHU	OpHV	OpHW	OpHX	OpHY	OpHZ	OpIA	OpIB	OpIC	OpID	OpIE	OpIF	OpIG	OpIH	OpII	OpIJ	OpIK	OpIL	OpIM	OpIN	OpIO	OpIP	OpIQ	OpIR	OpIS	OpIT	OpIU	OpIV	OpIW	OpIX	OpIY	OpIZ	OpJA	OpJB	OpJC	OpJD	OpJE	OpJF	OpJG	OpJH	OpJI	OpJJ	OpJK	OpJL	OpJM	OpJN	OpJO	OpJP	OpJQ	OpJR	OpJS	OpJT	OpJU	OpJV	OpJW	OpJX	OpJY	OpJZ	OpKA	OpKB	OpKC	OpKD	OpKE	OpKF	OpKG	OpKH	OpKI	OpKJ	OpKK	OpKL	OpKM	OpKN	OpKO	OpKP	OpKQ	OpKR	OpKS	OpKT	OpKU	OpKV	OpKW	OpKX	OpKY	OpKZ	OpLA	OpLB	OpLC	OpLD	OpLE	OpLF	OpLG	OpLH	OpLI	OpLJ	OpLK	OpLL	OpLM	OpLN	OpLO	OpLP	OpLQ	OpLR	OpLS	OpLT	OpLU	OpLV	OpLW	OpLX	OpLY	OpLZ	OpMA	OpMB	OpMC	OpMD	OpME	OpMF	OpMG	OpMH	OpMI	OpMJ	OpMK	OpML	OpMM	OpMN	OpMO	OpMP	OpMQ	OpMR	OpMS	OpMT	OpMU	OpMV	OpMW	OpMX	OpMY	OpMZ	OpNA	OpNB	OpNC	OpND	OpNE	OpNF	OpNG	OpNH	OpNI	OpNJ	OpNK	OpNL	OpNM	OpNN	OpNO	OpNP	OpNQ	OpNR	OpNS	OpNT	OpNU	OpNV	OpNW	OpNX	OpNY	OpNZ	OpOA	OpOB	OpOC	OpOD	OpOE	OpOF	OpOG	OpOH	OpOI	OpOJ	OpOK	OpOL	OpOM	OpON	OpOO	OpOP	OpOQ	OpOR	OpOS	OpOT	OpOU	OpOV	OpOW	OpOX	OpOY	OpOZ	OpPA	OpPB	OpPC	OpPD	OpPE	OpPF	OpPG	OpPH	OpPI	OpPJ	OpPK	OpPL	OpPM	OpPN	OpPO	OpPP	OpPQ	OpPR	OpPS	OpPT	OpPU	OpPV	OpPW	OpPX	OpPY	OpPZ	OpQA	OpQB	OpQC	OpQD	OpQE	OpQF	OpQG	OpQH	OpQI	OpQJ	OpQK	OpQL	OpQM	OpQN	OpQO	OpQP	OpQQ	OpQR	OpQS	OpQT	OpQU	OpQV	OpQW	OpQX	OpQY	OpQZ	OpRA	OpRB	OpRC	OpRD	OpRE	OpRF	OpRG	OpRH	OpRI	OpRJ	OpRK	OpRL	OpRM	OpRN	OpRO	OpRP	OpRQ	OpRR	OpRS	OpRT	OpRU	OpRV	OpRW	OpRX	OpRY	OpRZ	OpSA	OpSB	OpSC	OpSD	OpSE	OpSF	OpSG	OpSH	OpSI	OpSJ	OpSK	OpSL	OpSM	OpSN	OpSO	OpSP	OpSQ	OpSR	OpSS	OpST	OpSU	OpSV	OpSW
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0059	00	001D	00042	BNEQ	5\$		
00A1	0C	00A1	00044	INCL	GST_BEGIN_ADDR	4068	
00A1	00	00A1	00046	BRB	2\$		
		001D	00048	MOVL	R0, OLD_ADDRESS	4087	
		00A1	0004B	CASEB	(OLD_ADDRESS), #0, #12	4088	
		00A1	0004F	.WORD	6\$-4\$,-		
		008D	00057		8\$-4\$,-		
			0005F		9\$-4\$,-		
			00067		10\$-4\$,-		
					14\$-4\$,-		
					14\$-4\$,-		
					14\$-4\$,-		
					14\$-4\$,-		
					14\$-4\$,-		
					14\$-4\$,-		
					14\$-4\$,-		
					14\$-4\$,-		
					14\$-4\$,-		
					14\$-4\$,-		
					13\$-4\$		
		0084	31	BRW	14\$	4153	
50	52	52	C3	SUBL3	OLD_ADDRESS, OLD_ADDRESS, R0	4095	
	50	52	C0	ADDL2	OLD_ADDRESS, R0	4094	
	51	08	A2	MOVZBL	8(OLD_ADDRESS), R1	4096	
	50	51	C0	ADDL2	R1, R0		
	64	09	A0	MOVAB	9(R0), GST_BEGIN_ADDR		
			A3	BRB	2\$	4088	
50	52	52	C3	SUBL3	OLD_ADDRESS, OLD_ADDRESS, R0	4103	
	50	52	C0	ADDL2	OLD_ADDRESS, R0	4102	
	51	09	A2	MOVZBL	9(OLD_ADDRESS), R1	4104	
	50	51	C0	ADDL2	R1, R0		
	64	0A	A0	MOVAB	10(R0), GST_BEGIN_ADDR		
			44	BRB	12\$	4105	
50	52	52	C3	SUBL3	OLD_ADDRESS, OLD_ADDRESS, R0	4112	
	50	52	C0	ADDL2	OLD_ADDRESS, R0	4111	
	51	0B	A2	MOVZBL	11(OLD_ADDRESS), R1	4113	
	50	51	C0	ADDL2	R1, R0		
	64	0C	A0	MOVAB	12(R0), GST_BEGIN_ADDR		
			30	BRB	12\$	4114	
50	52	52	C3	SUBL3	OLD_ADDRESS, OLD_ADDRESS, R0	4123	
	50	52	C0	ADDL2	OLD_ADDRESS, R0	4122	
	51	0B	A2	MOVZBL	11(OLD_ADDRESS), R1	4124	
	50	51	C0	ADDL2	R1, R0		
	64	0C	A0	MOVAB	12(R0), GST_BEGIN_ADDR		
	50	64	D0	MOVL	GST_BEGIN_ADDR, R0	4125	
	53	01	A0	MOVZBL	1(R0), NUM_ARGS		
	64	02	C0	ADDL2	#2, GST_BEGIN_ADDR	4126	
			53	TSTL	NUM_ARGS	4127	
			10	BLEQ	12\$		
	51	64	D0	MOVL	GST_BEGIN_ADDR, R1	4130	
	50	01	A1	MOVZBL	1(R1), R0	4131	
	64	02	A140	MOVAB	2(R1)(R0), GST_BEGIN_ADDR		
			53	DECL	NUM_ARGS	4132	
			EC	BRB	11\$	4127	
	50	52	D0	MOVL	OLD_ADDRESS, R0	4134	
			04	RET			
		52	C3	SUBL3	OLD_ADDRESS, OLD_ADDRESS, R0	4141	
50	52	52	C0	ADDL2	OLD_ADDRESS, R0	4140	
	50	51	A2	MOVZBL	12(OLD_ADDRESS), R1	4142	

PATINT
V04-000

D 3
16-Sep-1984 01:02:56
14-Sep-1984 12:52:34

VAX-11 Bliss-32 V4.0-742
DISK\$VMSMASTER:[PATCH.SRC]PATINT.B32;1 (12)

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50		51	C0	000E7		ADDL2	R1, R0	:	
64	0D	A0	9E	000EA		MOVAB	13(R0), GST_BEGIN_ADDR	:	
		8E	11	000EE		BRB	7\$:	4088
64		01	CE	000F0	14\$:	MNEGL	#1, GST_BEGIN_ADDR	:	4148
		89	11	000F3		BRB	7\$:	4025
		50	D4	000F5	15\$:	CLRL	R0	:	4160
			04	000F7		RET		:	

; Routine Size: 248 bytes, Routine Base: _PAT\$CODE + 0505

```
1810 4161 1 GLOBAL ROUTINE PAT$RST_FREEZ ( UNITS ) =
1811 4162 1
1812 4163 1 ++
1813 4164 1 FUNCTIONAL DESCRIPTION:
1814 4165 1
1815 4166 1     Isolate storage allocation for the RST builder/manipulator.
1816 4167 1     i.e. Do exactly what PAT$FREEZ does for the rest of
1817 4168 1     PATCH, but take care of any differences (which may
1818 4169 1     or may not exist), when it is the RST interface
1819 4170 1     which wants the storage.
1820 4171 1
1821 4172 1     For now, there IS a difference - an RST-pointer is
1822 4173 1     returned, NOT the usual longword pointer. RST-pointers
1823 4174 1     are something internal to the RST builder/manipulator,
1824 4175 1     and it doesn't want to ever see anything but RST-pointers
1825 4176 1     (even if someday RST-pointers are the same thing as
1826 4177 1     virtual addresses). This is really the motivation for
1827 4178 1     having PAT$RST_FREEZ.
1828 4179 1
1829 4180 1 Formal Parameters:
1830 4181 1
1831 4182 1     UNITS - the number of units of storage which are
1832 4183 1     required. This unit will remain whatever
1833 4184 1     unit PAT$FREEZ knows about.
1834 4185 1
1835 4186 1 Implicit Inputs:
1836 4187 1
1837 4188 1     See PAT$FREEZ
1838 4189 1
1839 4190 1 Implicit Outputs:
1840 4191 1
1841 4192 1     See PAT$FREEZ
1842 4193 1
1843 4194 1 Routine Value:
1844 4195 1
1845 4196 1     0, if something goes wrong, an RST-pointer to the
1846 4197 1     allocated storage, otherwise.
1847 4198 1
1848 4199 1 Side Effects:
1849 4200 1
1850 4201 1     See PAT$FREEZ
1851 4202 1 --
1852 4203 1
1853 4204 2 BEGIN
1854 4205 2 LOCAL
1855 4206 2     STORAGE_PTR;
1856 4207 2
1857 4208 2 STORAGE_PTR = PAT$FREEZ( .UNITS );
1858 4209 2
1859 4210 2 ++
1860 4211 2     Currently an RST-pointer is just like a virtual
1861 4212 2     address except that the top 16 bits are 0 in the
1862 4213 2     former, and hex 7FFF0000 in the latter.
1863 4214 2     NOTE: THIS IS ONLY TRUE IF THE DEBUGGER INDICATOR IS TURNED OFF IN
1864 4215 2     PAT$FREEZ INIT. IF IT IS TURNED ON, THEN THE STORAGE IS OWN STORAGE, NOT
1865 4216 2     CONTAINED IN SYSTEM SPACE.
1866 4217 2 --
```


PATINT
V04-000

F 3
16-Sep-1984 01:02:56
14-Sep-1984 12:52:34

VAX-11 BLISS-32 V4.0-742
DISK\$VMSMASTER:[PATCH.SRC]PATINT.B32;1 (13)
Page 52

: 1867
: 1868
: 1869
4218 2 RETURN(.STORAGE_PTR - .PAT\$GL_RST_BEGN);
4219 2
4220 1 END;

00000000G EF 04 AC DD 00002
50 00000000G EF 01 FB 00005
C2 0000C
04 00013

.ENTRY PAT\$RST_FREEZ, Save nothing
PUSHL UNITS
CALLS #1, PAT\$FREEZ
SUBL2 PAT\$GL_RST_BEGN, R0
RET

: 4161
: 4208
: 4218
: 4220

; Routine Size: 20 bytes, Routine Base: _PAT\$CODE + 05FD

```
1871 4221 1 GLOBAL ROUTINE PAT$RST_RELEASE ( RST_PTR, SIZE ) : NOVALUE =
1872 4222 1
1873 4223 1 ++
1874 4224 1 FUNCTIONAL DESCRIPTION:
1875 4225 1
1876 4226 1 Isolate storage deallocation for all storage which
1877 4227 1 is accessed via RST-pointers.
1878 4228 1
1879 4229 1 i.e. Do exactly what PAT$FREERELEASE does for the rest of
1880 4230 1 PATCH, but take care of any differences (which may
1881 4231 1 or may not exist), when it is the RST interface
1882 4232 1 which wants to free up this special-access storage.
1883 4233 1
1884 4234 1 For now, there IS a difference - an RST-pointer is
1885 4235 1 given to indicate which storage to free up. This makes
1886 4236 1 PAT$RST_RELEASE the inverse of PAT$RST_FREEZ, just
1887 4237 1 as is true for the standard PATCH storage primitives.
1888 4238 1
1889 4239 1 Formal Parameters:
1890 4240 1
1891 4241 1 RST_PTR - this indicates which storage
1892 4242 1 is to be freed. This must be the same as
1893 4243 1 one which was returned by DBG$RST_FREEZ.
1894 4244 1 SIZE -The number of units which corresponds
1895 4245 1 to the storage to be freed.
1896 4246 1
1897 4247 1 Implicit Inputs:
1898 4248 1
1899 4249 1 See PAT$FREEZ
1900 4250 1
1901 4251 1 Implicit Outputs:
1902 4252 1
1903 4253 1 See PAT$FREEZ
1904 4254 1
1905 4255 1 Routine Value
1906 4256 1
1907 4257 1 NOVALUE
1908 4258 1
1909 4259 1 Side Effects:
1910 4260 1
1911 4261 1 See PAT$FREEZ
1912 4262 1 --
1913 4263 1
1914 4264 2 BEGIN
1915 4265 2
1916 4266 2 ++
1917 4267 2 Currently an RST-pointer is just like a virtual
1918 4268 2 address except that the top 16 bits are 0 in
1919 4269 2 in the former and hex 7FFF0000 in the latter.
1920 4270 2 --
1921 4271 2 PAT$FREERELEASE( .RST_PTR + .PAT$GL_RST_BEGN, .SIZE );
1922 4272 1 END;
```

PATINT
V04-000

H 3
16-Sep-1984 01:02:56
14-Sep-1984 12:52:34

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DISK\$VMSMASTER:[PATCH.SRC]PATINT.B32;1 (14)

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0000 00000
08 AC DD 00002
7E 04 AC 00000000G EF C1 00005
00000000G EF 02 FB 0000E
04 00015

.ENTRY PAT\$RST_RELEASE, Save nothing : 4221
PUSHL SIZE : 4271
ADDL3 PAT\$GL_RST_BEGN, RST_PTR, -(SP)
CALLS #2, PAT\$FREERELEASE :
RET : 4272

; Routine Size: 22 bytes, Routine Base: _PAT\$CODE + 0611

PATINT
V04-000

1 3
16-Sep-1984 01:02:56
14-Sep-1984 12:52:34

VAX-11 Bliss-32 V4.0-742
DISK\$VMSMASTER:[PATCH.SRC]PATINT.B32;1 (15)

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: 1924 4273 1 END
: 1925 4274 0 ELUDOM

! End of module

.EXTRN LIB\$SIGNAL

PSECT SUMMARY

Name	Bytes	Attributes
PAT\$OWN	44	NOVEC, WRT, RD, NOEXE, NOSHR, LCL, REL, CON, NOPIC, ALIGN(2)
PAT\$CODE	1575	NOVEC, NOWRT, RD, EXE, NOSHR, LCL, REL, CON, NOPIC, ALIGN(2)
ABS	0	NOVEC, NOWRT, NORD, NOEXE, NOSHR, LCL, ABS, CON, NOPIC, ALIGN(0)
PAT\$PLIT	8	NOVEC, NOWRT, RD, NOEXE, NOSHR, LCL, REL, CON, NOPIC, ALIGN(0)

Library Statistics

File	----- Total	Symbols Loaded	----- Percent	Pages Mapped	Processing Time
_\$255\$DUA28:[SYSLIB]LIB.L32;1	18619	32	0	1000	00:01.8

: Information: 1
: Warnings: 0
: Errors: 0

COMMAND QUALIFIERS

: BLISS/CHECK=(FIELD,INITIAL,OPTIMIZE)/VARIANT:1/LIS=LIS\$:PATINT/OBJ=OBJ\$:PATINT MSRC\$:PATINT/UPDATE=(ENH\$:PATINT)

: Size: 1575 code + 52 data bytes
: Run Time: 00:47.7
: Elapsed Time: 02:43.1
: Lines/CPU Min: 5378
: Lexemes/CPU-Min: 30094
: Memory Used: 252 pages
: Compilation Complete

0301 AH-BT13A-SE
VAX/VMS V4.0

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0302 AH-BT13A-SE
VAX/VMS V4.0

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